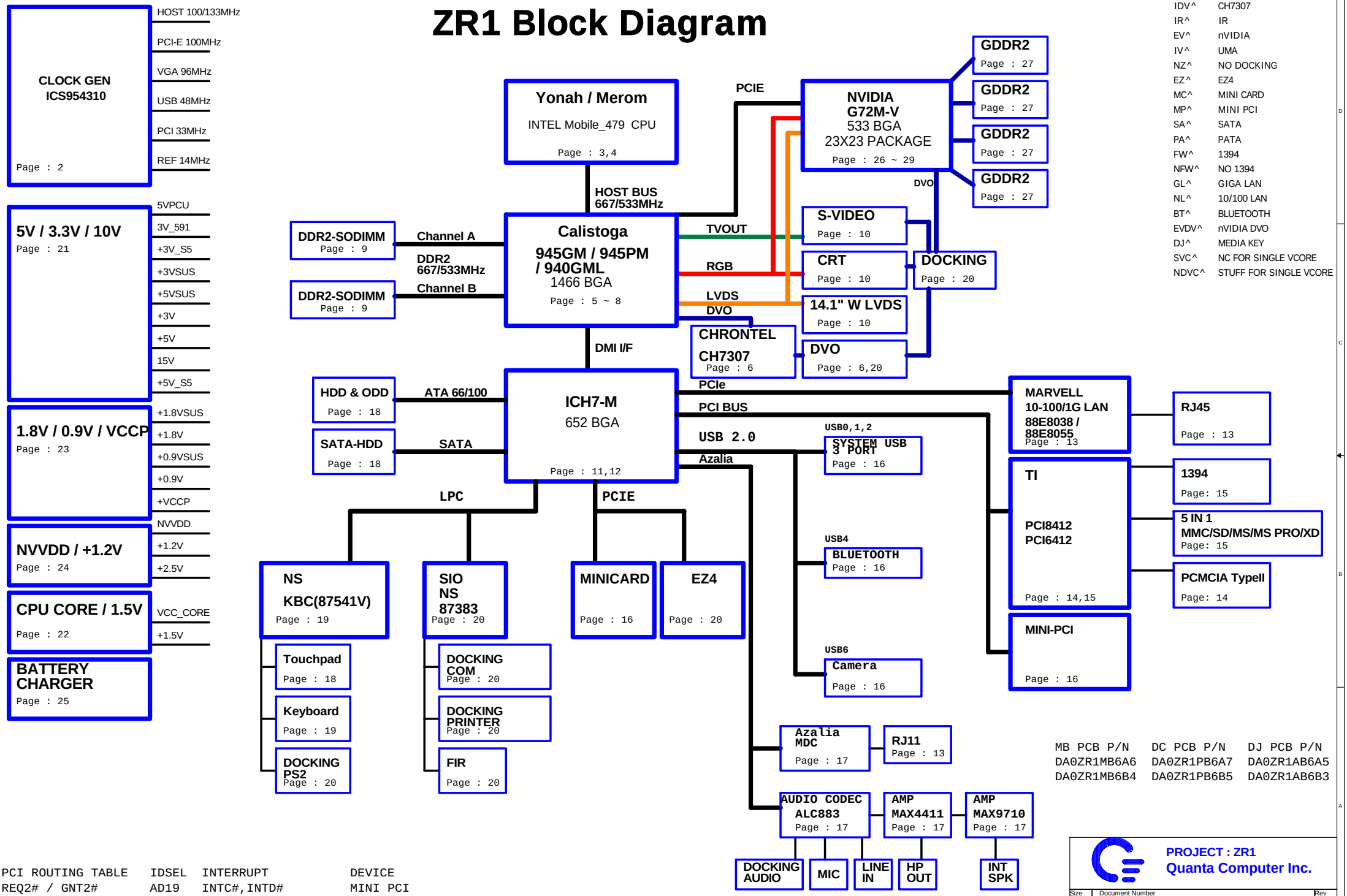
ZR1 Block Diagram



IDV^	CH7307
IR^	IR
EV^	nVIDIA
IV^	UMA
NZ^	NO DOCKING
EZ^	EZ4
MC^	MINI CARD
MP^	MINI PCI
SA^	SATA
PA^	PATA
FW^	1394
NFW^	NO 1394
GL^	GIGA LAN
NL^	10/100 LAN
BT^	BLUETOOTH
EVDV^	nVIDIA DVO
DJ^	MEDIA KEY
SVC^	NC FOR SINGLE VCORE
NDVC^	STUFF FOR SINGLE VCORE

MB PCB P/N	DC PCB P/N	DJ PCB P/N
DA0ZR1MB6A6	DA0ZR1PB6A7	DA0ZR1AB6A5
DA0ZR1MB6B4	DA0ZR1PB6B5	DA0ZR1AB6B3

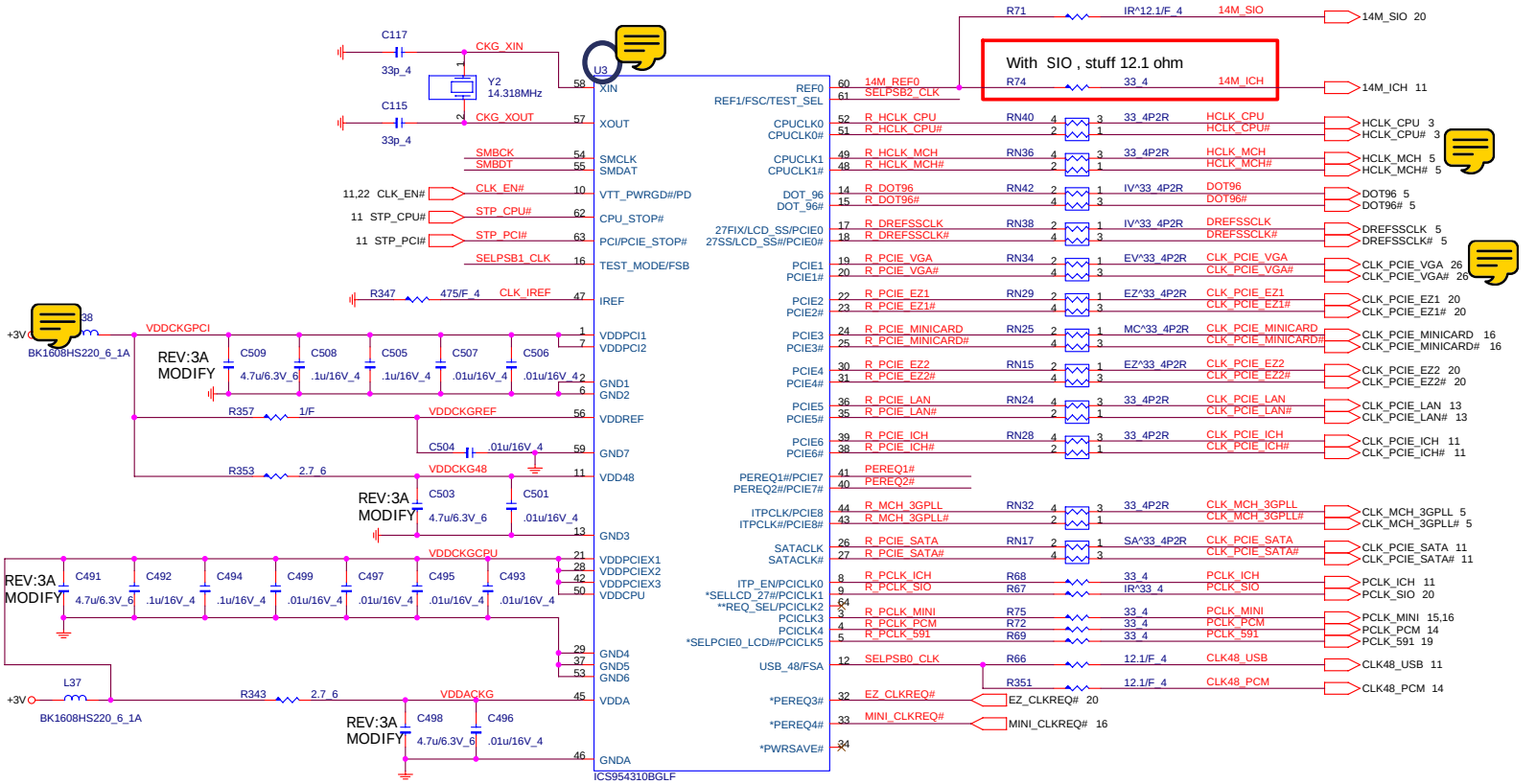
PCI ROUTING TABLE	IDSEL	INTERRUPT	DEVICE
REQ2# / GNT2#	AD19	INTC#,INTD#	MINI PCI
REQ0# / GNT0#	AD25	INTE#,INTF#,INTG#	TI XX12

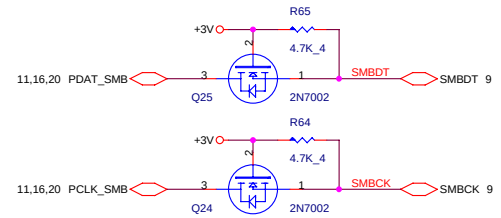
PROJECT : ZR1
Quanta Computer Inc.

Size	Document Number	Rev
	BLOCK DIAGRAM	1A

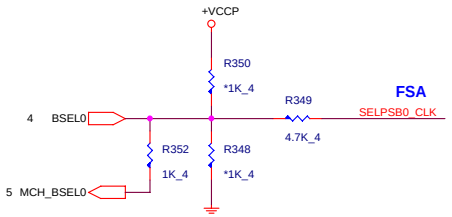
Date: Wednesday, August 02, 2006 Sheet 1 of 30

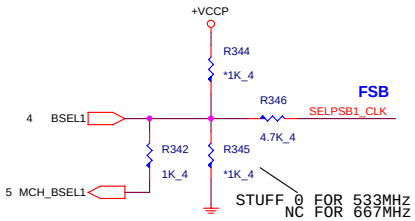
CLOCK GENERATOR

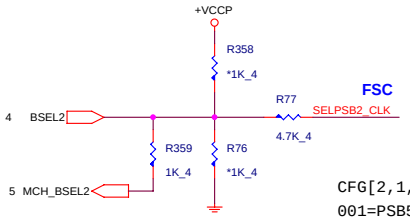




SMBUS Address : D2 (read) , D3 (write)

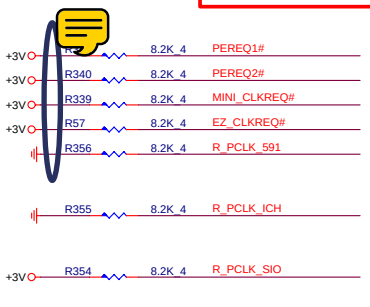


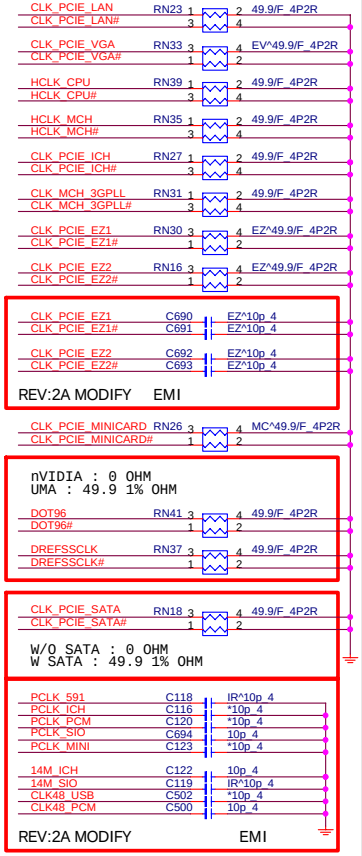




CFG[2,1,0]
001=PSB533
011=PSB667

PEREQ1# --> PCIE0 & PCIE6
PEREQ2# --> PCIE1 & PCIE8
PEREQ3# --> PCIE2 & PCIE4
PEREQ4# --> PCIE3 & PCIE5 & PCIE7





FSB SETTING

FSC	FSB	FSA	CPU	PCIE	PCI
0	0	0	266	100	33
0	0	1	133	100	33
0	1	0	200	100	33
0	1	1	166	100	33
1	0	0	333	100	33
1	0	1	100	100	33
1	1	0	400	100	33
1	1	1	200	100	33

PROJECT : ZR1
Quanta Computer Inc.

Size	Document Number	Rev
	CLOCK GENERATOR	3A

Date: Wednesday, August 02, 2006 Sheet 2 of 30

CPU

U32A

YONAH/MEROM

1 OF 3

479P_SOCKET

REQUEST PHASE SIGNALS

DATA PHASE SIGNALS

ERROR SIGNALS

ARBITRATION PHASE SIGNALS

SNOOP PHASE SIGNALS

RESPONSE PHASE SIGNALS

PC COMPATIBILITY SIGNALS

DIAGNOSTIC & TEST SIGNALS

EXECUTION CONTROL SIGNALS

THERMAL DIODE

REV:3A MODIFY

REV:2A MODIFY

125 degree protection

SMBUS Address : 98

MAXIM 6657 : AL006657020

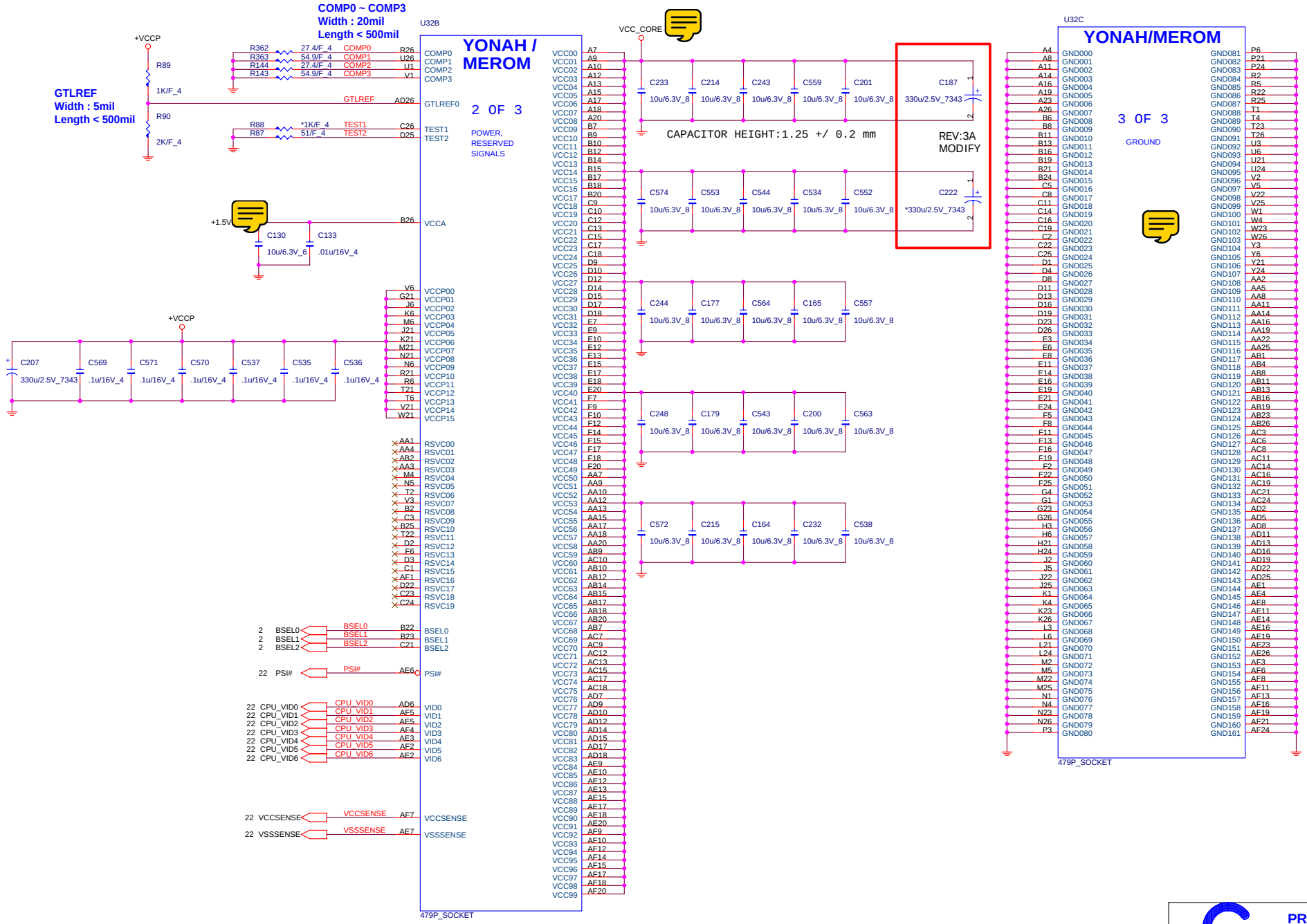
GMT G781 : AL000781101

Width : 10mil
Length < 500mil

REV:2A MODIFY

PROJECT : ZR1		
Quanta Computer Inc.		
Size	Document Number: CPU (HOST)	Rev: 3A
Date: Wednesday, August 02, 2006	Sheet 3 of 30	

CPU

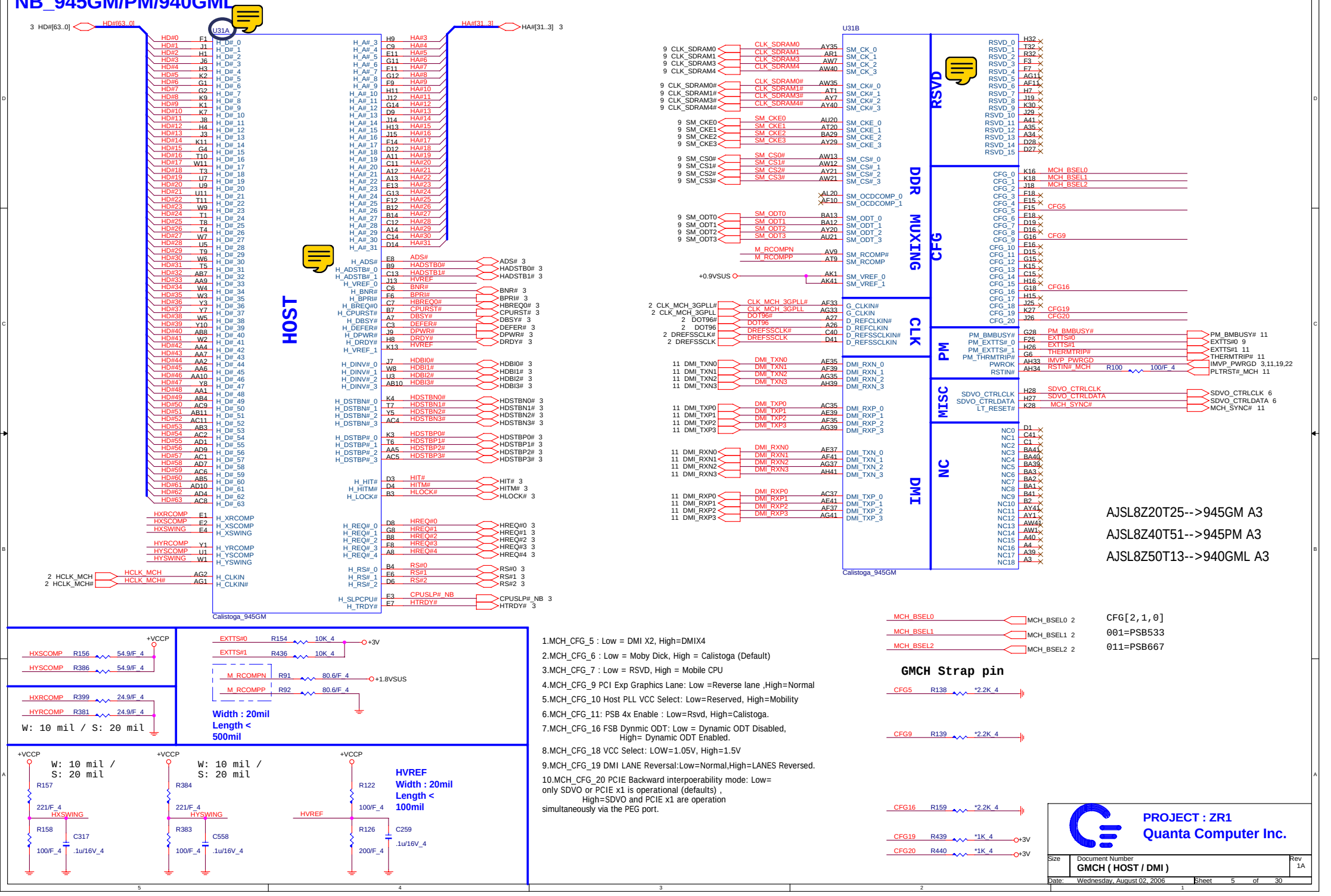


PROJECT : ZR1
Quanta Computer Inc.

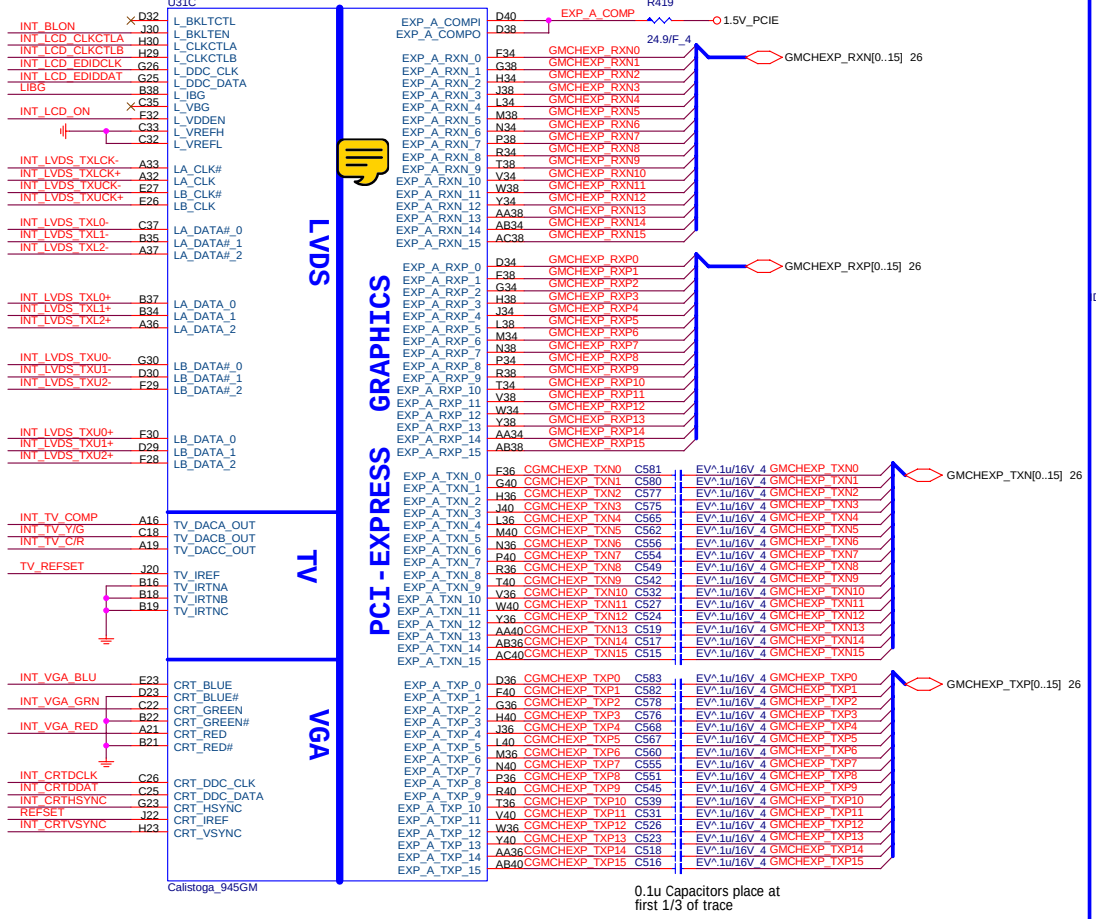
Size	Document Number	Rev
	CPU (POWER)	3A

Date: Wednesday, August 02, 2006 Sheet 4 of 30

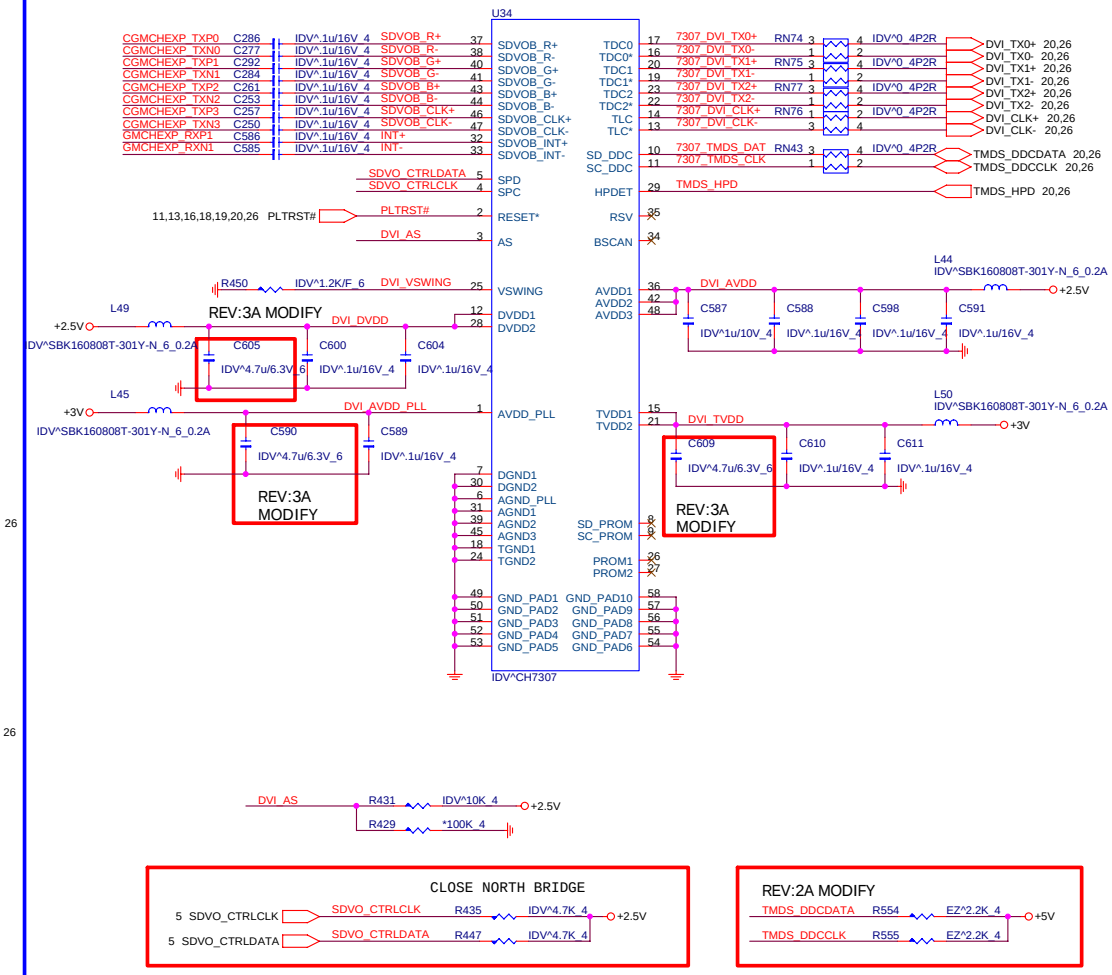
NB_945GM/PM/940GML

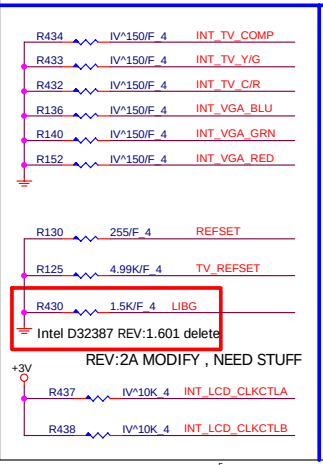


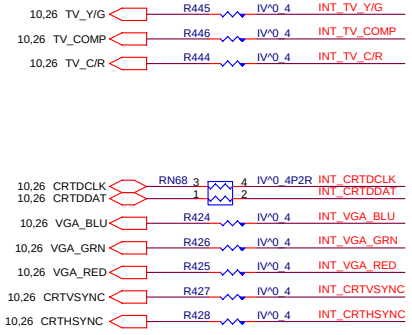
NB_945GM/PM/940GML

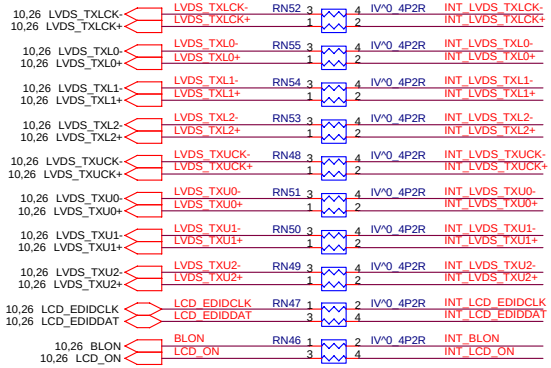


DVO_CH7307

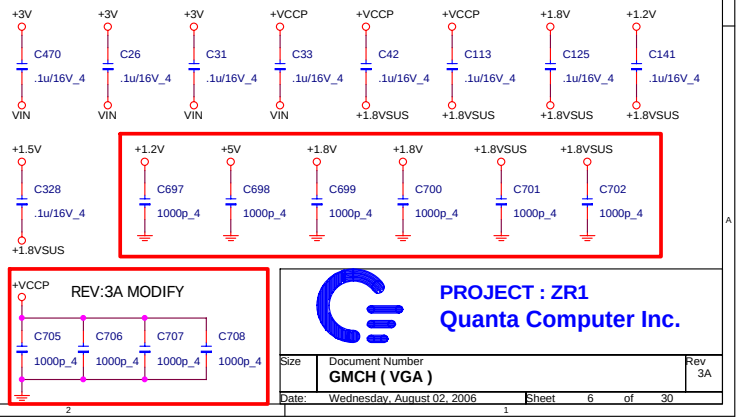








Signal Cross Moat

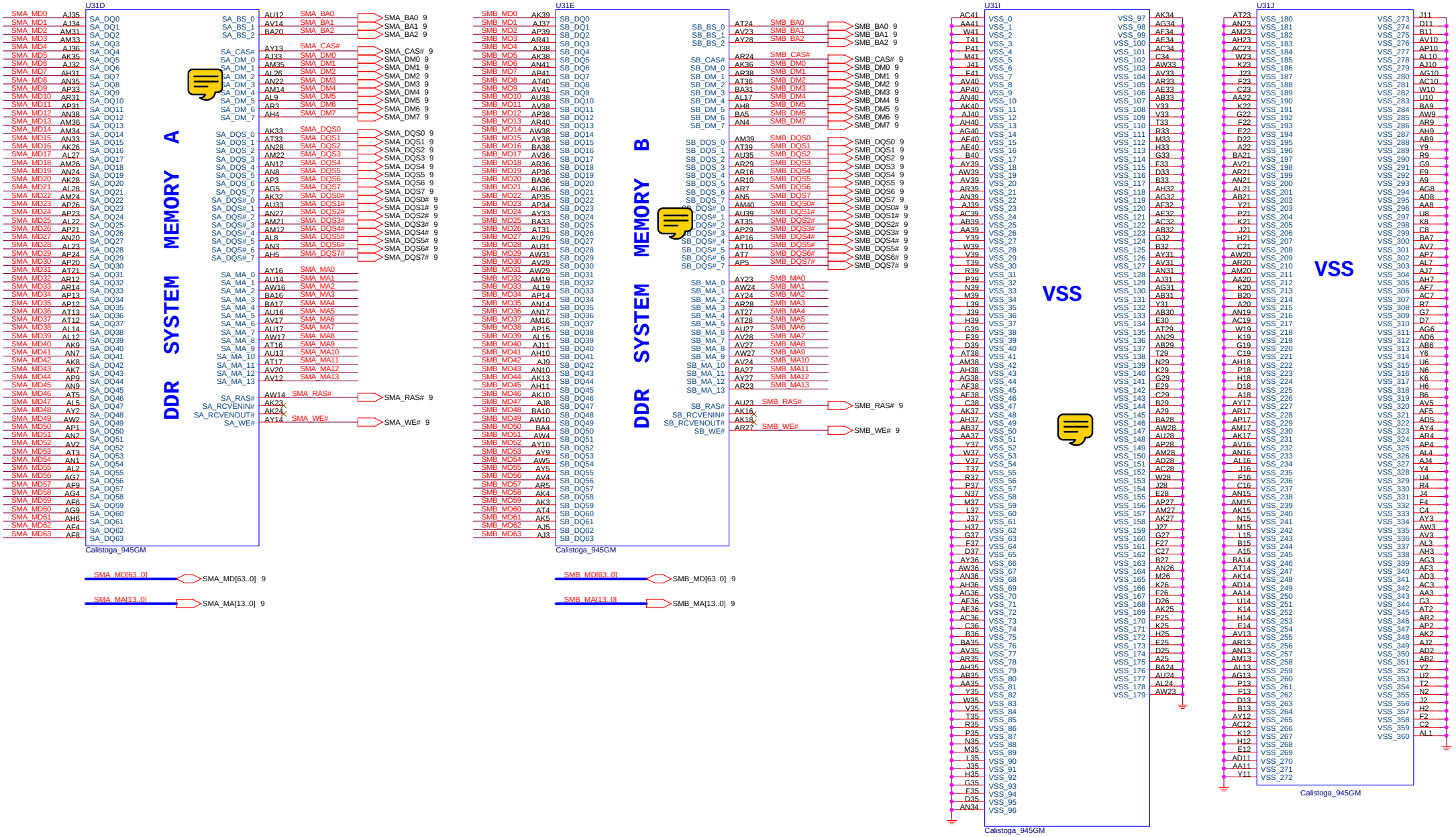


PROJECT : ZR1
Quanta Computer Inc.

Size	Document Number	Rev
	GMCH (VGA)	3A

Date: Wednesday, August 02, 2006 Sheet 6 of 30

NB_945GM/PM/940GML

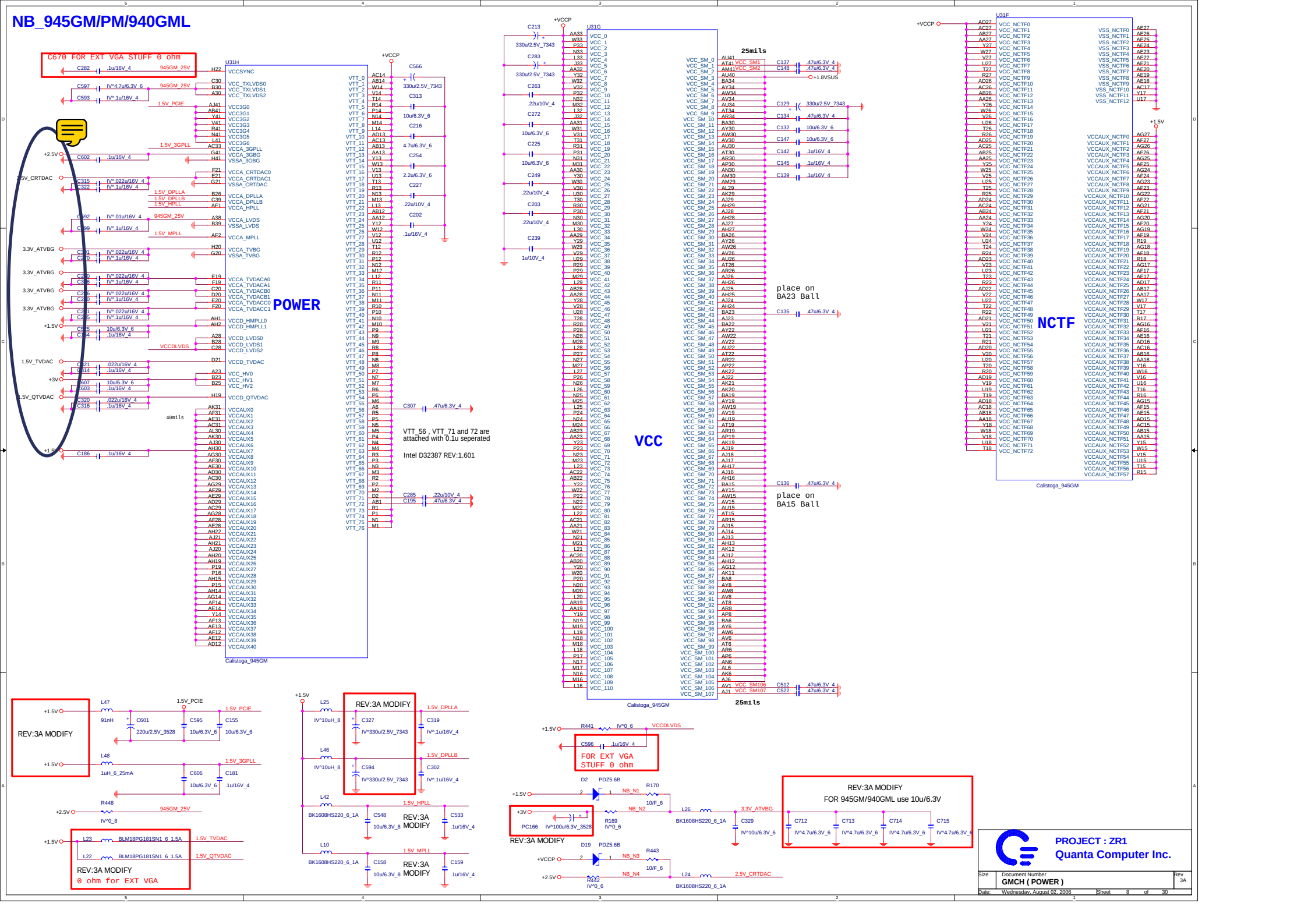


PROJECT : ZR1
Quanta Computer Inc.

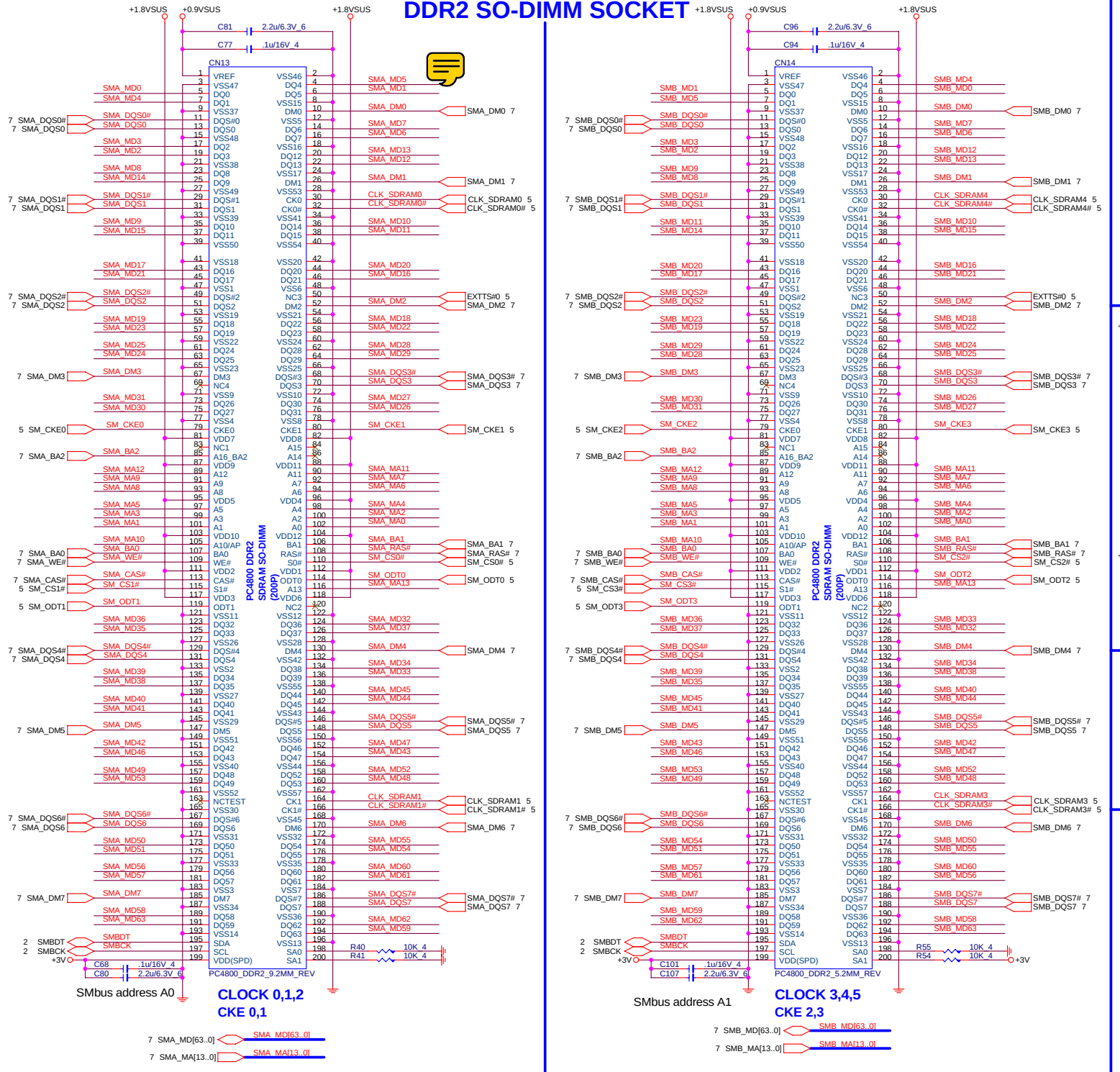
Size	Document Number	Rev
	GMCH (MEMORY)	1A

Date: Wednesday, August 02, 2006 Sheet 7 of 30

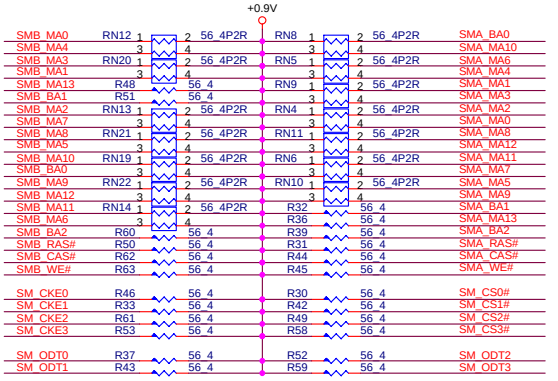
NB_945GM/PM/940GML



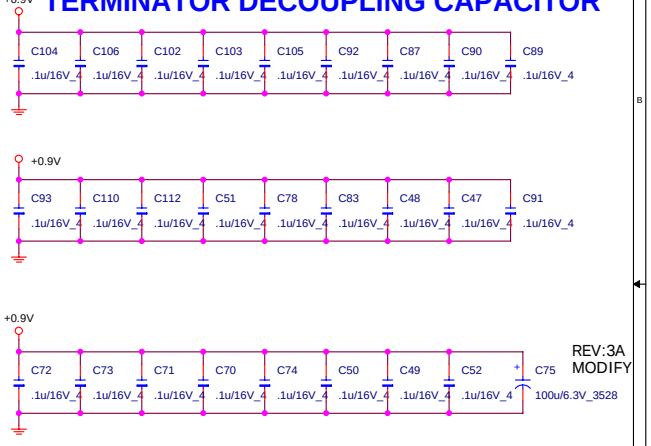
DDR2 SO-DIMM SOCKET



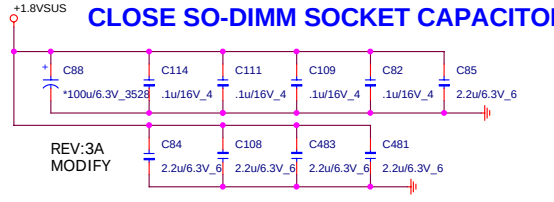
DDR2 TERMINATOR



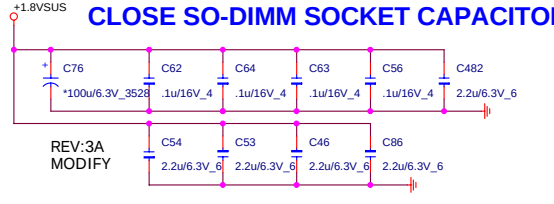
TERMINATOR DECOUPLING CAPACITOR



CLOSE SO-DIMM SOCKET CAPACITORS



CLOSE SO-DIMM SOCKET CAPACITORS

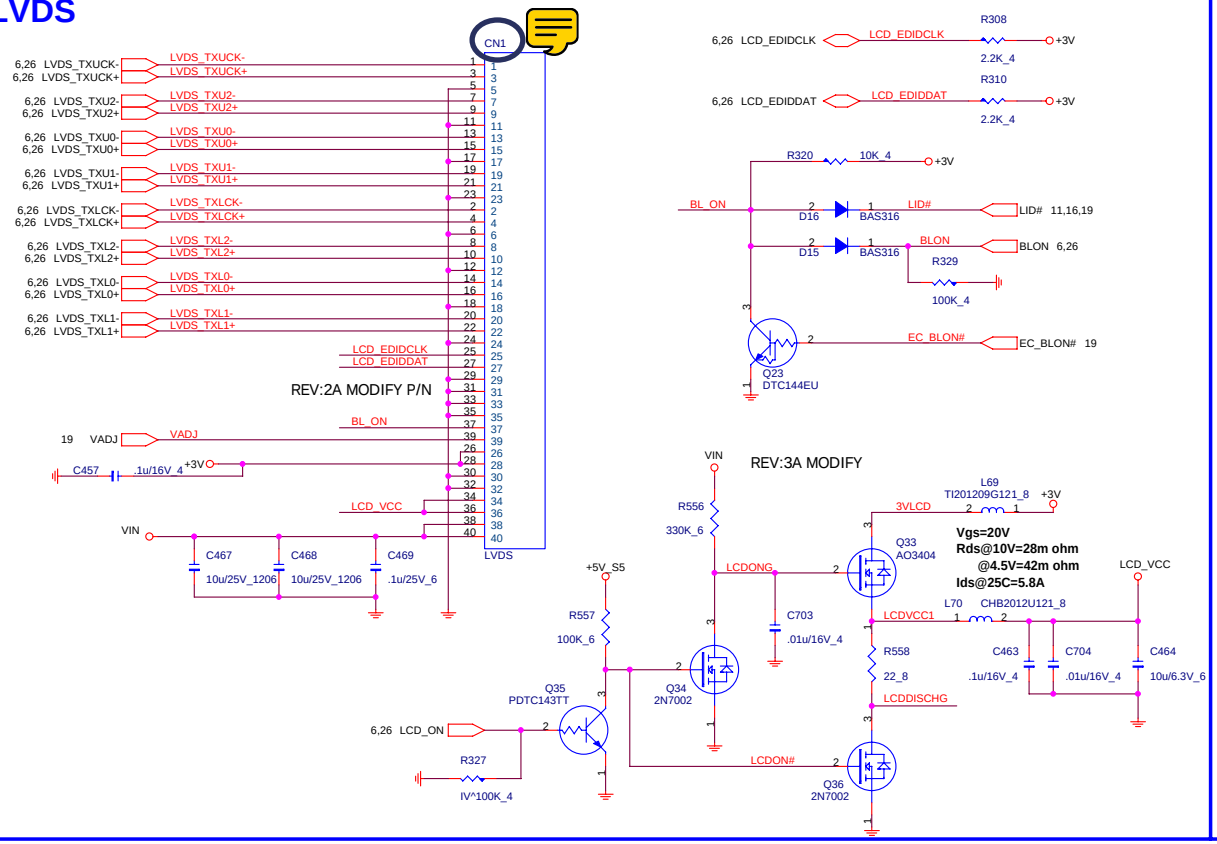


PROJECT : ZR1
Quanta Computer Inc.

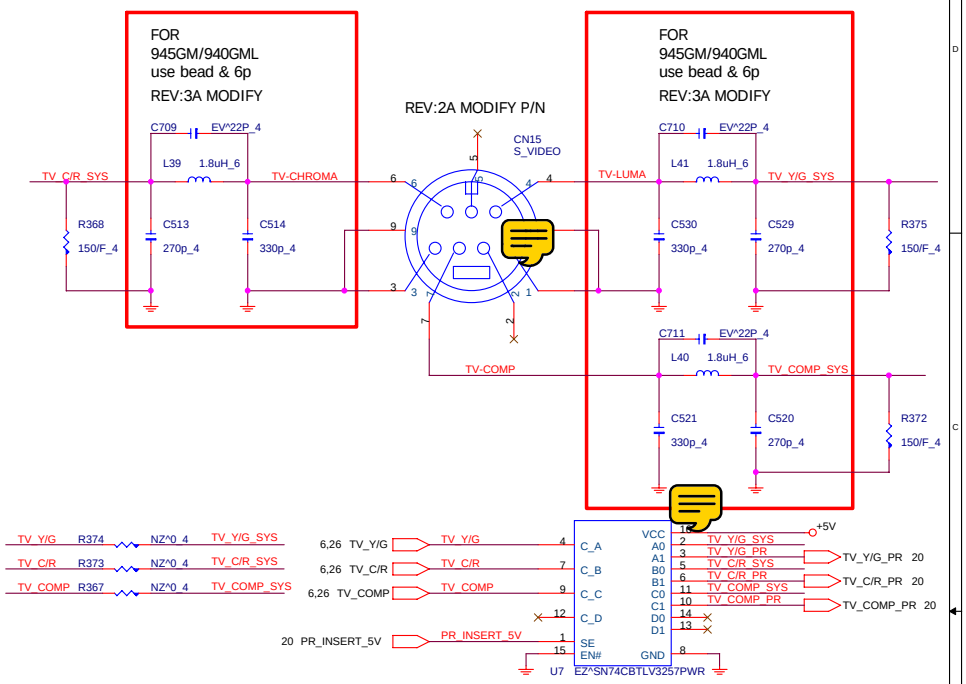
Size	Document Number	Rev
	DDR SO-DIMM(200P)	3A

Date: Wednesday, August 02, 2006 Sheet 9 of 30

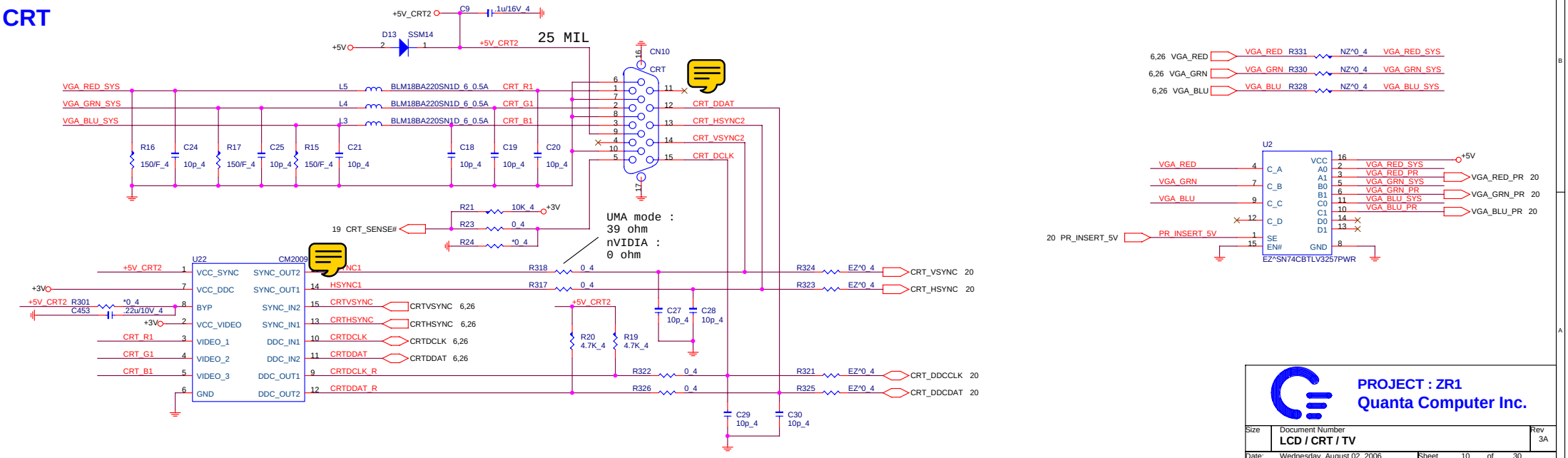
LVDS



S-VIDEO

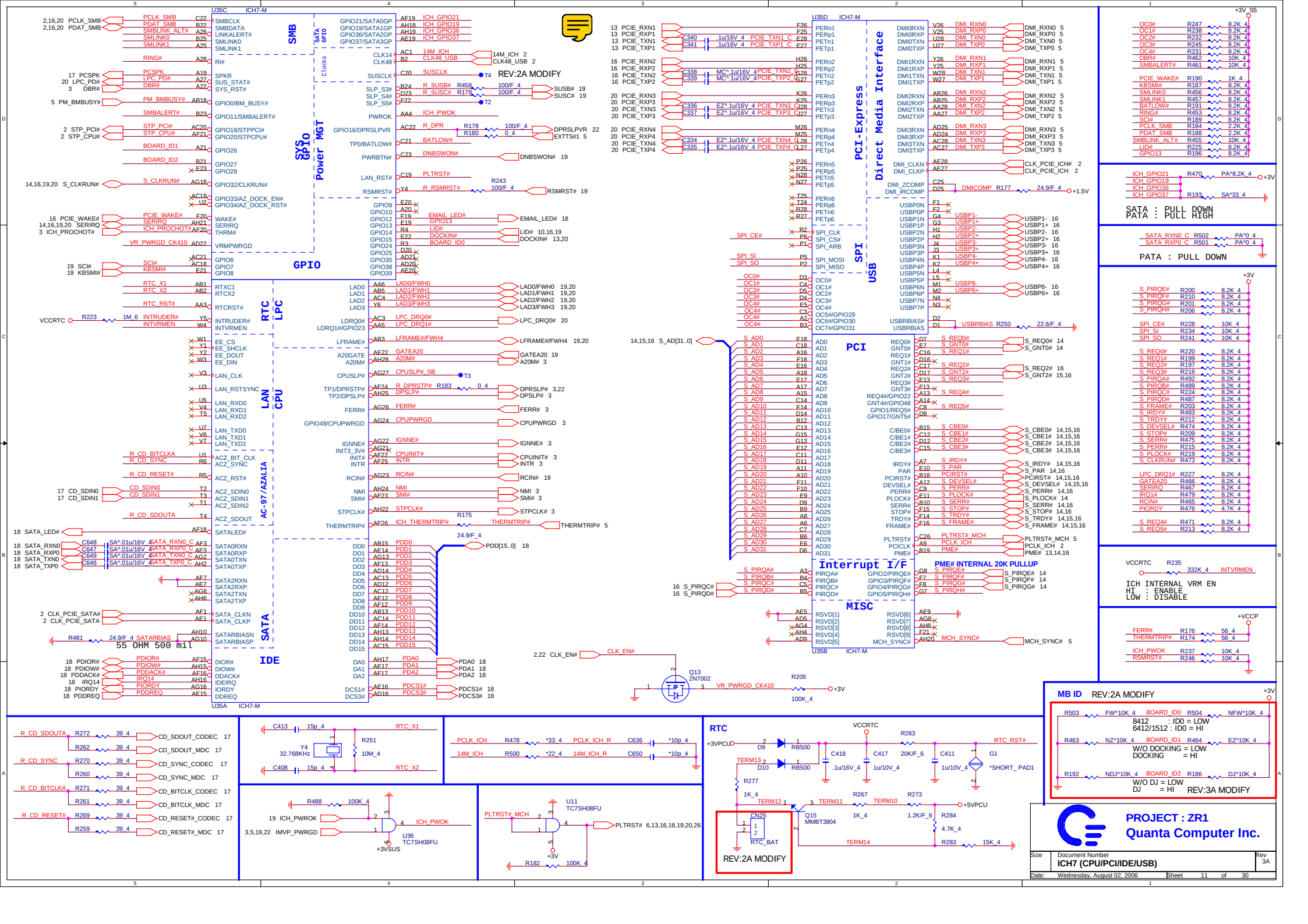


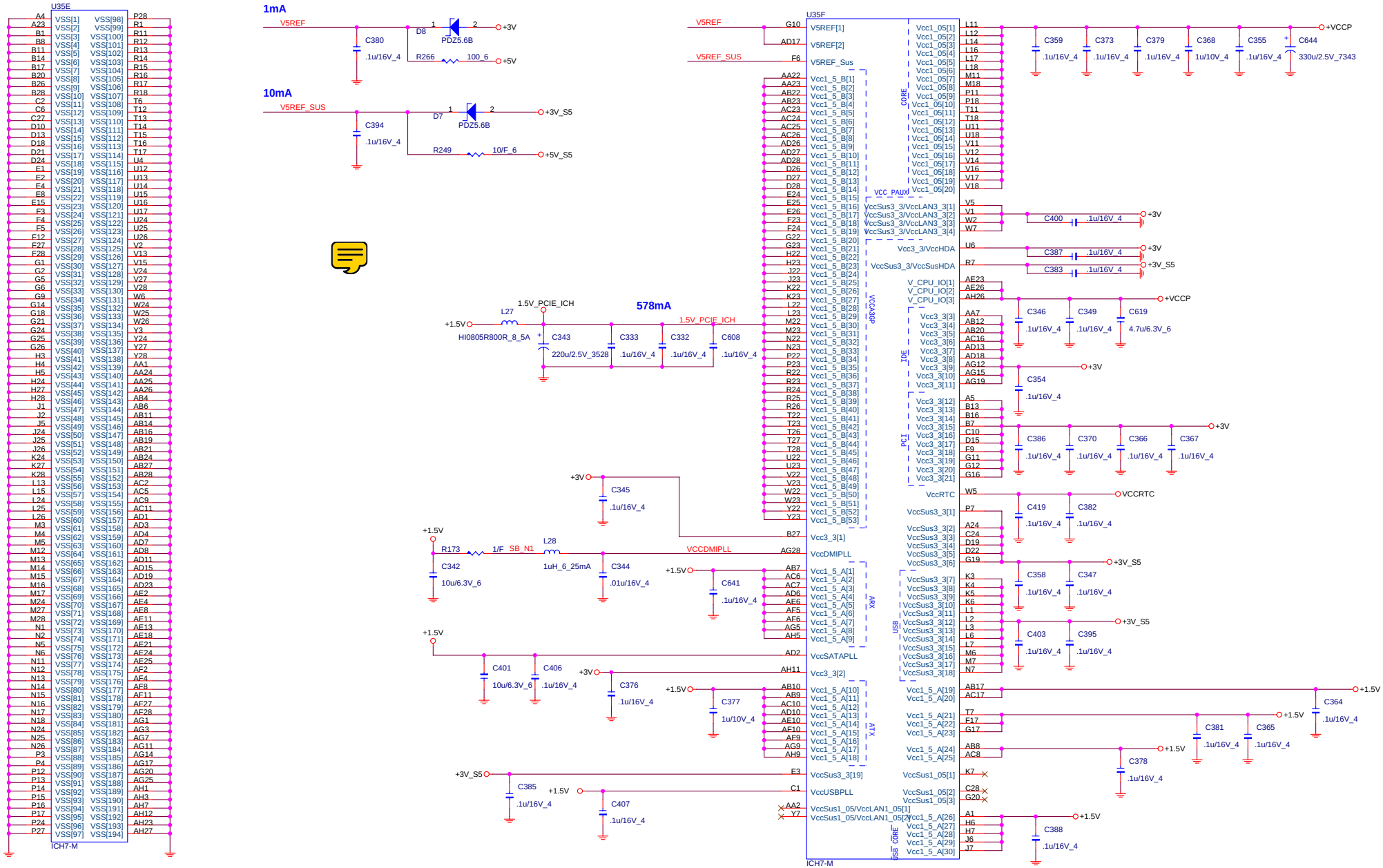
CRT



PROJECT : ZR1
Quanta Computer Inc.

Size	Document Number	Rev
	LCD / CRT / TV	3A
Date: Wednesday, August 02, 2006	Sheet 10 of 30	


U35C ICH7-M
SMB
SMBCLK
SMBDATA
LINKALERT#
SMLINK0
SMLINK1
SYS GPIO Power MGT
GPIO
RTC LPC
LAN CPU
AC-97/AZALIA
SATA
IDE
U35A ICH7-M
U35D ICH7-M
PCI-Express
Direct Media Interface
SPI
USB
PCI
Interrupt I/F
MISC
U35B ICH7-M
REV:2A MODIFY
PME# INTERNAL 20K PULLUP
55 OHM 500 mil
SATA : PULL DOWN
PATA : PULL HIGH
PATA : PULL DOWN
ICH INTERNAL VRM EN
HI : ENABLE
LOW : DISABLE
MB ID
8412 : ID0 = LOW
6412/1512 : ID0 = HI
W/O DOCKING = LOW
DOCKING = HI
W/O DJ = LOW
DJ = HI
REV:3A MODIFY
RTC
REV:2A MODIFY
PROJECT : ZR1
Quanta Computer Inc.
ICH7 (CPU/PCI/IDE/USB)
Wednesday, August 02, 2006

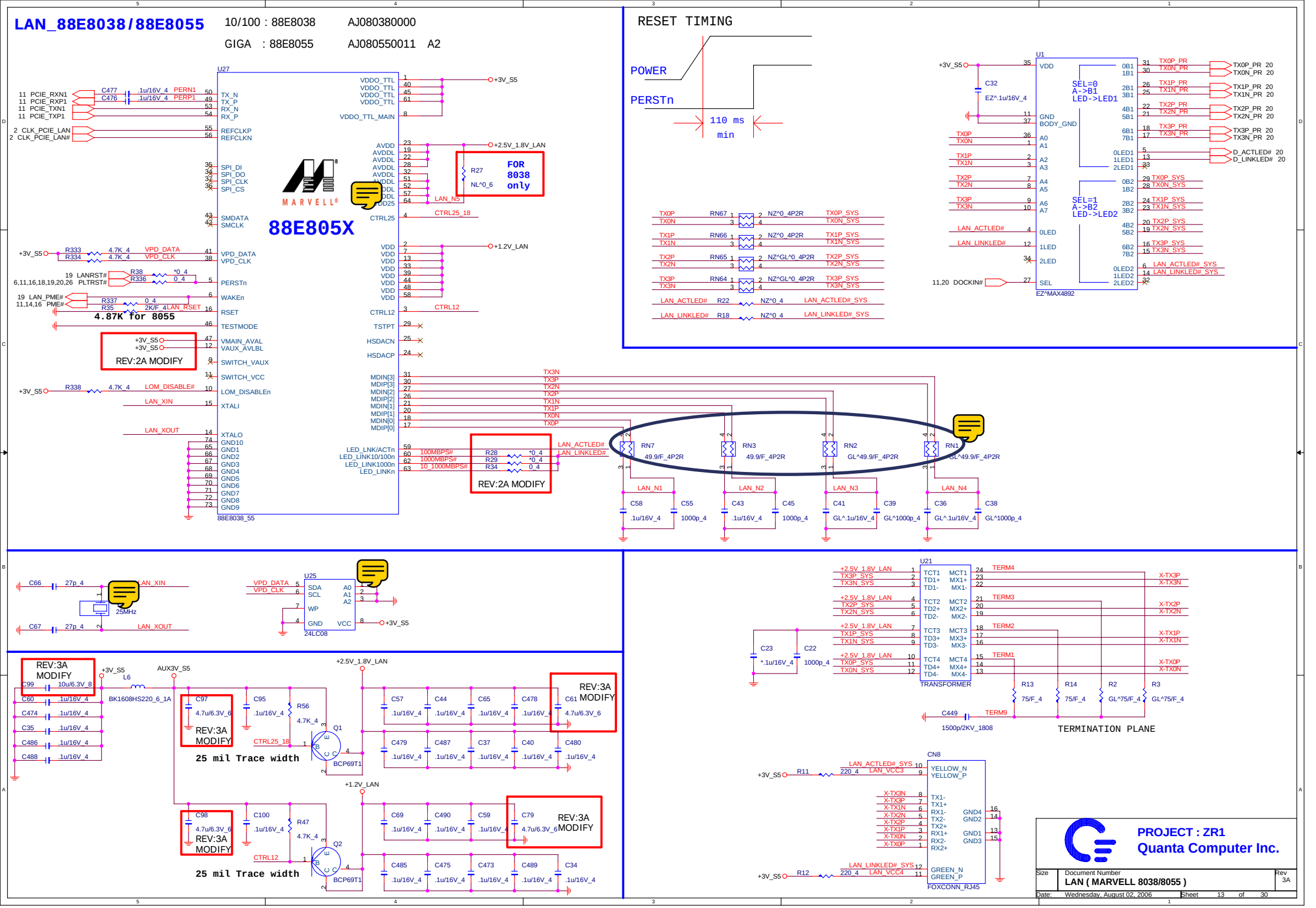


PROJECT : ZR1			
Quanta Computer Inc.			
Size	Document Number		Rev
	ICH7 (POWER)		1A
Date:	Wednesday, August 02, 2006	Sheet 12 of 30	

LAN_88E8038/88E8055

10/100 : 88E8038 AJ080380000

GIGA : 88E8055 AJ080550011 A2



PCIXX12_PCMCIA

PCI8412 : AJ084120T08 PCMCIA / 1394 / 5 IN 1

PCI6412 : AL064120T04 PCMCIA / 5 IN 1

REQ0# AD25
GNT0# PIRQ(E,F,G)#

S_AD25 R255 150/F_4 PCM_IDSEL

11,15,16 S_AD[31..0] S_AD[31..0]

U14A PCIXX12

S_AD0 R11 AD0
S_AD1 P11 AD1
S_AD2 U11 AD2
S_AD3 V11 AD3
S_AD4 W11 AD4
S_AD5 R10 AD5
S_AD6 U10 AD6
S_AD7 V10 AD7
S_AD8 R9 AD8
S_AD9 U9 AD9
S_AD10 V9 AD10
S_AD11 W9 AD11
S_AD12 V8 AD12
S_AD13 U8 AD13
S_AD14 R8 AD14
S_AD15 W7 AD15
S_AD16 W4 AD16
S_AD17 T2 AD17
S_AD18 T1 AD18
S_AD19 R3 AD19
S_AD20 P5 AD20
S_AD21 R2 AD21
S_AD22 R1 AD22
S_AD23 P3 AD23
S_AD24 N3 AD24
S_AD25 N2 AD25
S_AD26 N1 AD26
S_AD27 M5 AD27
S_AD28 M6 AD28
S_AD29 M3 AD29
S_AD30 M2 AD30
S_AD31 M1 AD31

11,15,16 S_CBE0# S_CBE0# W10 C/BE0
11,15,16 S_CBE1# S_CBE1# V7 C/BE1
11,15,16 S_CBE2# S_CBE2# U5 C/BE2
11,15,16 S_CBE3# S_CBE3# P2 C/BE3

11,16 S_PAR S_PAR U7 PAR
11,15,16 S_DEVSEL# S_DEVSEL# U6 DEVSEL
11,15,16 S_FRAME# S_FRAME# R6 FRAME
11 S_GNT0# S_GNT0# L2 GNT
PCM_IDSEL N5 IDSEL
11,15,16 S_IRDY# S_IRDY# V5 IRDY
11,16 S_PERR# S_PERR# R7 PERR
11 S_REQ0# S_REQ0# L3 REQ
11,16 S_SERR# S_SERR# W6 SERR
11,16 S_STOP# S_STOP# V6 STOP
11,15,16 S_TRDY# S_TRDY# W5 TRDY

CLK48 F1 → REV:3A MODIFY → CLK48_PCM 2
GRST K5 GRST# 8402
SUSPEND J5 8402_SUS# R490 10K_4 +3V
MFUNC0 G1 S_PIRQE# → S_PIRQE# 11
MFUNC1 H5 S_PIRQF# → S_PIRQF# 11
MFUNC2 H2 S_PIRQG# → S_PIRQG# 11
MFUNC3 H1 SERIRQ → SERIRQ 11,16,19,20
MFUNC4 J1 S_PLOCK# → S_PLOCK# 11
MFUNC5 J2
MFUNC6 J3 → S_CLKRUN# 11,16,19,20
PCLK L1 PCLK_PCM → PCLK_PCM 2
PRST K3 PCIRST# → PCIRST# 11,15,16
SPKROUT H3 PCMSPK → PCMSPK 17
RI_OUT/PME L5 → REV:3A MODIFY → PME# 11,13,16
SDA G3 SDA_CARD
SCL G2 SCL_CARD
LATCH C9 TPS_LATCH
DATA B9 TPS_DATA
CLOCK A9 TPS_CLK
USB_EN E10
PHY_TEST_MA P17 TEST_MA R468 4.7K_4 1394_AVDD
TEST0 P12
VSVD // VD0 // VCCD1# C4 PSMODE R233 10K_4 +3V
NC R5

PCLK_PCM R254 *22_4 PCLK_PCM_R C416 *10p_4
CLK48_PCM R253 *22_4 CLK48_PCM_R C415 *10p_4

A_VCC
C375 .1u/16V_4
C356 .01u/16V_4

U14B PCIXX12

PCMCIA PORT

VCCB A15
VCCB J19

CAD31 // D10 C10 A_CAD31
CAD30 // D9 A10 A_CAD30
CAD29 // D1 F11 A_CAD29
CAD28 // D8 E11 A_CAD28
CAD27 // D0 C11 A_CAD27
CAD26 // A0 B13 A_CAD26
CAD25 // A1 C13 A_CAD25
CAD24 // A2 A14 A_CAD24
CAD23 // A3 B14 A_CAD23
CAD22 // A4 B15 A_CAD22
CAD21 // A5 E14 A_CAD21
CAD20 // A6 A16 A_CAD20
CAD19 // A25 D19 A_CAD19
CAD18 // A7 F17 A_CAD18
CAD17 // A24 E15 A_CAD17
CAD16 // A17 H19 A_CAD16
CAD15 // IOWR# J17 A_CAD15
CAD14 // A9 J15 A_CAD14
CAD13 // IORD# J18 A_CAD13
CAD12 // A11 K15 A_CAD12
CAD11 // OE# K17 A_CAD11
CAD10 // CE2# K18 A_CAD10
CAD9 // A10 L15 A_CAD9
CAD8 // D15 L18 A_CAD8
CAD7 // D7 L19 A_CAD7
CAD6 // D13 M17 A_CAD6
CAD5 // D6 M18 A_CAD5
CAD4 // D12 N19 A_CAD4
CAD3 // D5 M15 A_CAD3
CAD2 // D11 N17 A_CAD2
CAD1 // D4 N18 A_CAD1
CAD0 // D3 P19 A_CAD0

CC/BE3# // REG# F13 A_CC/BE3#
CC/BE2# // A12 E18 A_CC/BE2#
CC/BE1# // A8 H18 A_CC/BE1#
CC/BE0# // CE1# L17 A_CC/BE0#

CPAR // A13 H14 A_CPAR
CDEVSEL# // A21 F19 A_CDEVSEL#
CFRAME# // A23 E19 A_CFRAME#
CGNT# // WE# G17 A_CGNT#
CINT# // READY(IREQ#) E12 A_CINT#
CIRDY# // A15 F17 A_CIRDY#
CPERR# // A14 G19 A_CPERR#
CREQ# // INPACK# C14 A_CREQ#
CSERR# // WAIT# C12 A_CSERR#
CSTOP# // A20 G18 A_CSTOP#
CTRDY# // A22 G15 A_CTRDY#

CCLK // A16 F18 R_A_CCLK
CCLKRUN# // WP(IOIS16#) A11 A_CCLKRUN#
CSTSCHG // BVD1(STSCHG#/RI#) A12 A_CSTSCHG
CBLOCK# // A19 H15 A_CBLOCK#
CRST# // RESET C15 A_CRST#
CAUDIO // BVD2(SPKR#) B12 A_CAUDIO

CCD1# // CD1# N15 A_CCD1#
CCD2# // CD2# B11 A_CCD2#
CVS1 // VS1# A13 A_CVS1#
CVS2 // VS2# B16 A_CVS2#

RSVD // A18 H17 A_CRSVD/A18
RSVD // D2 B10 A_CRSVD/D2
RSVD // D14 M19 A_CRSVD/D14

R_A_CCLK R244 33_4 A_CCLK

CN23 PCMCIA_SOCKET

GND1 1 A_CAD0 2 A_CAD1 3 A_CAD3 4 A_CAD5 5 A_CAD7 6 A_CC/BE0# 7 A_CAD9 8 A_CAD11 9 A_CAD12 10 A_CAD14 11 A_CC/BE1# 12 A_CPAR 13 A_CPERR# 14 A_CGNT# 15 A_CINT# 16

UPPER PIN

A_CCLK 19 A_CIRDY# 20 A_CC/BE2# 21 A_CAD18 22 A_CAD20 23 A_CAD21 24 A_CAD22 25 A_CAD23 26 A_CAD24 27 A_CAD25 28 A_CAD26 29 A_CAD27 30 A_CAD29 31 A_CRSVD/D2 32 A_CCLKRUN# 33 GND2 34

GND3 35 A_CCD1# 36 A_CAD2 37 A_CAD4 38 A_CAD6 39 A_CRSVD/D14 40 A_CAD8 41 A_CAD10 42 A_CVS1# 43 A_CAD13 44 A_CAD15 45 A_CAD16 46 A_CRSVD/A18 47 A_CBLOCK# 48 A_CSTOP# 49 A_CDEVSEL# 50

LOWER PIN

A_CTRDY# 53 A_CFRAME# 54 A_CAD17 55 A_CAD19 56 A_CVS2# 57 A_CRST# 58 A_CSERR# 59 A_CREQ# 60 A_CC/BE3# 61 A_CAUDIO 62 A_CSTSCHG 63 A_CAD28 64 A_CAD30 65 A_CAD31 66 A_CCD2# 67 GND4 68

SKTA/VCC1 17 SKTA/VCC2 51 → A_VCC
SKTA/VPP1 18 SKTA/VPP2 52 → AVPP
GND5 69 GND6 70 GND7 71 GND8 72 GND9 73 GND10 74

GND1 SKTAAD0/D3 SKTAAD1/D4 SKTAD3/D5 SKTAD5/D6 SKTAAD7/D7 -SKTACBE0/CE1# SKTAAD9/A10 SKTAAD11/OE# SKTAAD12/A11 SKTAAD14/A9 -SKTACBE1/A8 SKTAPAR/A13 -SKTAPERR/A14 -SKTAGNT/WE# -SKTAINT/RDY

SKTAPCLK/A16 -SKTAIRDY/A15 -SKTACBE2/A12 SKTAAD18/A7 SKTAAD20/A6 SKTAAD21/A5 SKTAAD22/A4 SKTAAD23/A3 SKTAAD24/A2 SKTAAD25/A1 SKTAAD26/A0 SKTAAD27/D0 SKTAAD29/D1 SKTARSVD/D2 -SKTACLKRUN/WP GND2

GND3 -SKTACD1/CD1# SKTAAD2/D11 SKTAD4/D12 SKTAAD6/D13 SKTARSVD/D14 SKTAAD8/D15 SKTAAD10/CE2# -SKTAVS1/VS1# SKTAAD13/IORD# SKTAAD15/IOWR# SKTAD16/A17 -SKTRSVD/A18 -SKTALOCK/A19 -SKTASTOP/A20 -SKTADEVSEL/A21

-SKTATRDY/A22 -SKTAFRAME/A23 SKTAAD17/A24 SKTAAD19/A25 -SKTAVS2VS2# -SKTARST/RESET 0SKTASERR/WAIT# -SKTAREQ/INPACK# -SKTACBE3/REG# SKTAAUDIO/BVD2 -SKTASTSCHG/BVD1 SKTAAD28/D8 SKTAAD30/D9 SKTAAD31/D10 -SKTACD2/CD2# GND4

+3V
R265 FW^2.2K_4
R252 FW^2.2K_4
R274 NFW^2.2K_4
R258 NFW^2.2K_4
SDA_CARD 5 SDA
SCL_CARD 6 SCL
WP 7
GND 4
U16 FW^24LC08
A0 1
A1 2
A2 3
VCC 8 +3V
C422 FW^.1u/16V_4

+3V
R482 22K_4
GRST# 8402
C639 .22u/10V_4

> 2 ms
> 0 ns
VCC
GRST#
PRST#
> 100 us
PCLK

REV:3A MODIFY

U15 TPS2220A

TPS_DATA 3 DATA
TPS_CLK 4 CLOCK
TPS_LATCH 5 LATCH
PCIRST# 12 RESET#
21 SHDN#
15 OC#
8 AVPP
10 AVCC1
9 AVCC0
19 BVPP
18 BVCC1
17 BVCC0
5V_0 1
5V_1 2
5V_2 24
3.3VIN0 14
3.3VIN1 13
12V_0 7
12V_1 20
BODY 25
NC_0 6
NC_1 16
NC_2 22
NC_3 23
GND 11
+5V
+3V
AVPP
A_VCC
C405 4.7u/6.3V_6
C402 .1u/16V_4
C390 4.7u/6.3V_6
C392 .1u/16V_4
C412 4.7u/6.3V_6
C410 .1u/16V_4
C391 4.7u/6.3V_6
C389 .1u/16V_4

PROJECT : ZR1
Quanta Computer Inc.

Size	Document Number	Rev
	PCIXX12 (PCMCIA)	3A
Date: Wednesday, August 02, 2006	Sheet 14 of 30	

MINI-PCI

REQ2# AD19
GNT2# PIRQ(C,D)#

S_AD19 R282 MP^150/F_4 MINI_IDSEL

PCLK_MINI R286 *22_4 C435 *22p_4

S_AD[31..0] S_AD[31..0] 11,14,15

CN27 MP^PTWO_MINIPCI_H6.8

D11 BAS316

18 RF_LED
19 RF_ENABLE
RF_EN_R
11 S_PIRQD#
2,15 PCLK_MINI
11 S_REQ2#
11,14,15 S_CBE3#
11,14,15 S_CBE2#
11,14,15 S_IRDY#
11,14,19,20 S_CLKRUN#
11,14 S_SERR#
11,14 S_PERR#
11,14,15 S_CBE1#

TIP LAN1 LAN3 LAN5 LAN7 LED_GP LED_GN NC1 -INTB +3V R(IRQ3) GND PCICLK GND -REQ +3V AD31 AD29 GND AD27 AD25 (V) -CBE3 AD23 GND AD21 AD19 GND AD17 -CBE2 -IRDY +3V -CLKRUN -SERR GND -PERR -CBE1 AD14 GND AD12 AD10 GND AD8 AD7 +3V AD5 (V) AD3 +5V AD1 GND SYNC SDIN0 BITCLK -AC_PRIMARY BEEP AGND +MIC -MIC AGND -RI +5VA

RING LAN2 LAN4 LAN6 LAN8 LED_YP LED_YN NC2 +5V -INTA R(IRQ4) +3VAUX -RST +3V -GNT GND -PME (V) AD30 +3V AD28 AD26 AD24 IDSEL GND AD22 AD20 PAR AD18 AD16 GND -FRAME -TRDY -STOP +3V -DEVSEL GND AD15 AD13 AD11 GND AD9 -CBE0 +3V AD6 AD4 AD2 AD0 (V) SERIRQ GND M66EN SDOUT SDIN1 -RESET -MPCICACK AGND +SPK -SPK AGND NC4 +3VAUX

S_PIRQC# S_PIRQC# 11
PCIRST# PCIRST# 11,14,15
S_GNT2# S_GNT2# 11,15
PME# PME# 11,13,14
S_AD30
S_AD28
S_AD26
S_AD24
MINI_IDSEL
S_AD22
S_AD20
S_PAR S_PAR 11,14
S_AD18
S_AD16
S_FRAME# S_FRAME# 11,14,15
S_TRDY# S_TRDY# 11,14,15
S_STOP# S_STOP# 11,14
S_DEVSEL# S_DEVSEL# 11,14,15
S_AD15
S_AD13
S_AD11
S_AD9
S_CBE0# S_CBE0# 11,14,15
S_AD6
S_AD4
S_AD2
S_AD0
SERIRQ SERIRQ 11,14,19,20

C438 C420 C436 C437 MP^.1u/16V_4

C662 C425 C424 C426 MP^.1u/16V_4

USB

REV:3A Modify

11 USBP1-
11 USBP1+
RN90 0_4P2R
L43
*DLW21HN900SQ2L
BUSBP1-
BUSBP1+

USBPWRP1 CN17 USB
L12 TI201209G121_8_3A USBPWR1
C561 470p_4
C546 100u/6.3V_3528

+5V_S5 U9 G548A2P8U
19 USBON#
R109 *6.34K/F_4

DC/B CONNECTOR

USB PORT
USB PORT
USB Camera

CN11 B2B

11 USBP3-
11 USBP3+
11 USBP2-
11 USBP2+
11 USBP6-
11 USBP6+
VA VA 20,25
+5V_S5
+3V
USBON# USBON# 19
CCD_PWRON# CCD_PWRON# 19
LID# LID# 10,11,19
NBSWON# NBSWON# 19

MINI-CARD

CN21 MC^MINI CARD

11 PCIE_TXP2
11 PCIE_TXN2
11 PCIE_RXP2
11 PCIE_RXN2
C628 C629 MC^.1u/16V_4
PERP2
PERN2
2 CLK_PCIE_MINICARD
2 CLK_PCIE_MINICARD#
2 MINI_CLKREQ#
11 PCIE_WAKE#

Reserved +3.3V GND +1.5V LED_WPAN# LED_WLAN# LED_WWAN# GND USB_D+ USB_D- GND SMB_DATA SMB_CLK +1.5V GND +3.3Vaux PERST# Reserved GND Reserved Reserved Reserved Reserved +1.5V GND +3.3V

PETp0 PETn0 PERp0 PERn0 REFCLK+ REFCLK- CLKREQ# WAKE#

WL_VCC1
RF_LED 18
PDAT_SMB 2,11,20
PCLK_SMB 2,11,20
RF_EN_R
PLTRST# 6,11,13,18,19,20,26
WL_VCC2

BLUETOOTH

+3VSUS Q12 BT^AO3403 BT_POWER
C617 BT^4.7u/6.3V_6
19 BT_PWRON#
11 USBP4+
11 USBP4-
REV:3A Modify
RN91 BT^0_4P2R
L51 *DLW21HN900SQ2L
BUSBP4+
BUSBP4-
CN19 BT^USB_BT
+1.8VSUS
18 BT_LED

+3V R547 MC^0_6 WL_VCC1 C689 MC^.1u/16V_4
R548 MC^0_6 WL_VCC2 C614 MC^.1u/16V_4
REV:2A MODIFY

+3V_S5 C635 MC^.1u/16V_4
+1.5V C616 MC^.1u/16V_4 C638 MC^.1u/16V_4

PROJECT : ZR1
Quanta Computer Inc.

Size	Document Number	Rev
	MINI_CARDPCI/USB/BT/DC	3A
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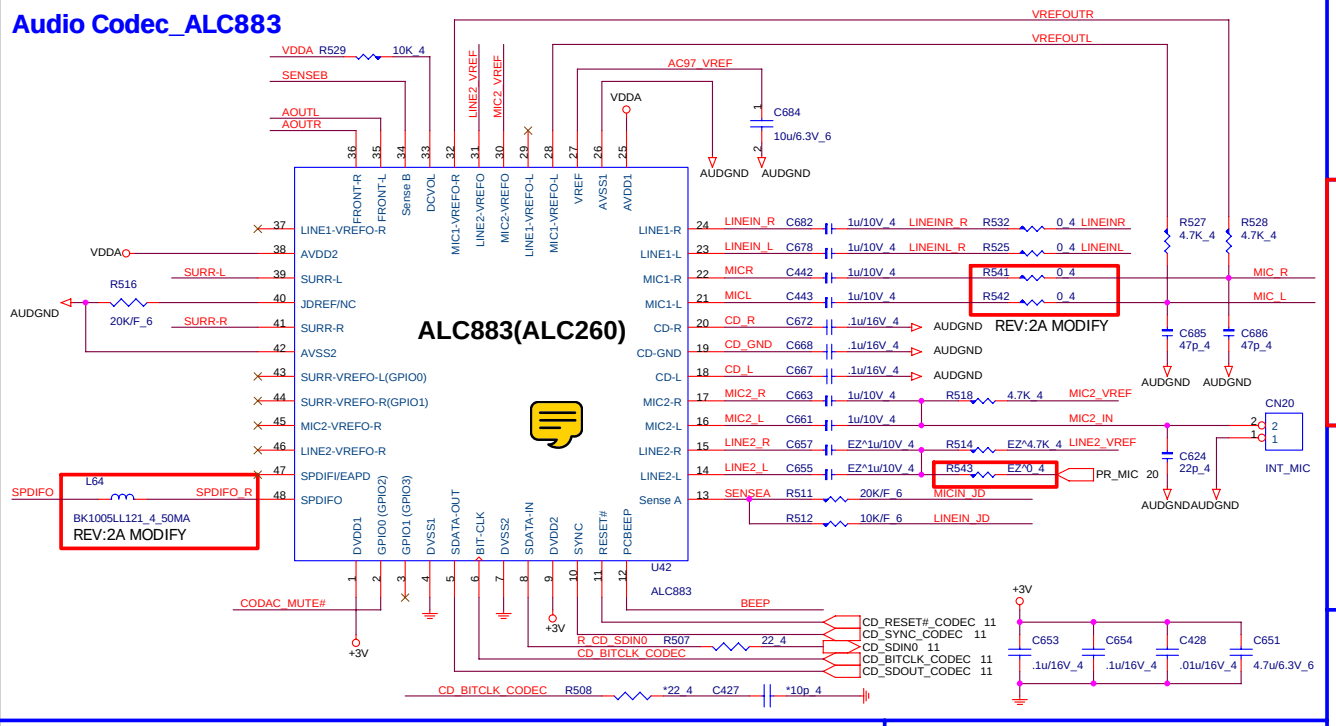
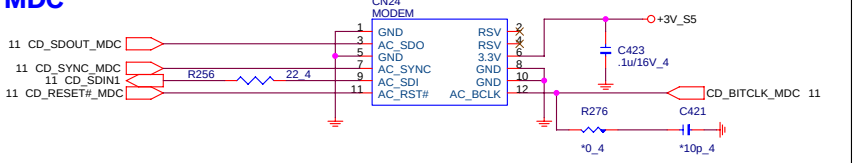
Audio Codec_ALC883

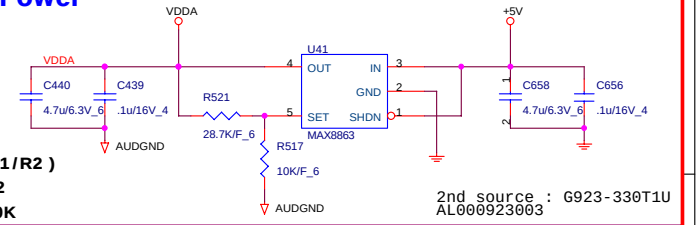


MDC



Audio Codec Power

REV:2A MODIFY

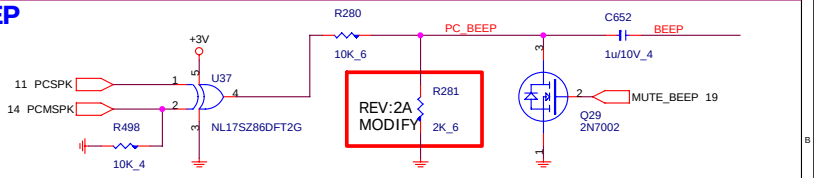


VOUT = 1.25 (1+R1/R2)

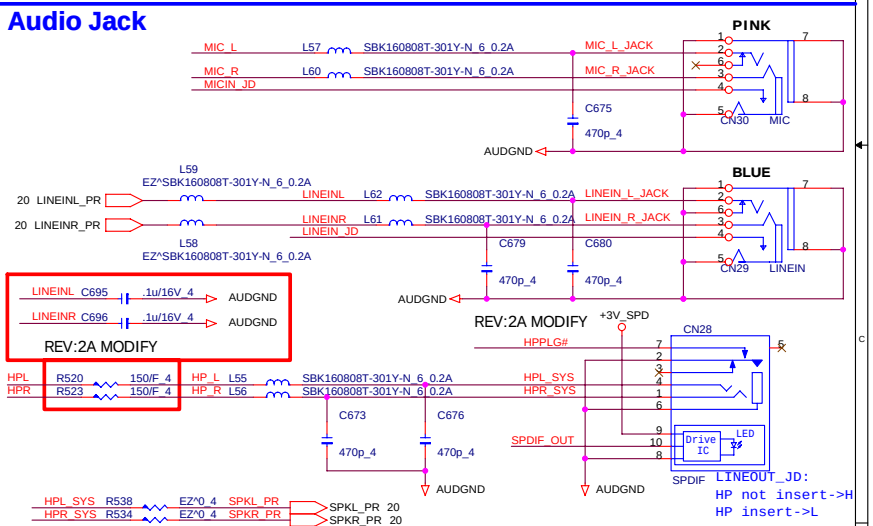
R1	R2
4.8375V	28.7K 10K

2nd source : G923-330T1U
AL000923003

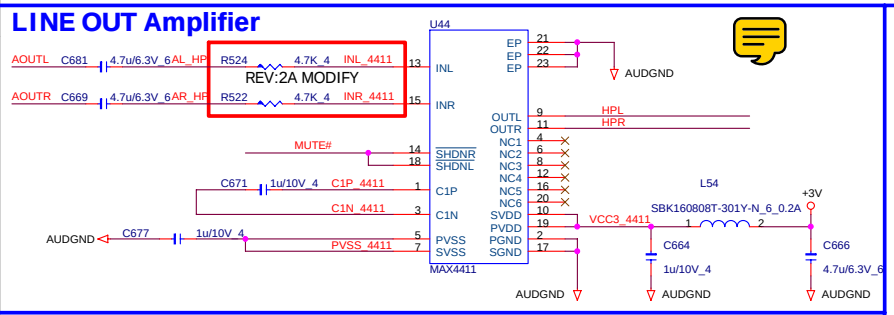
BEEP

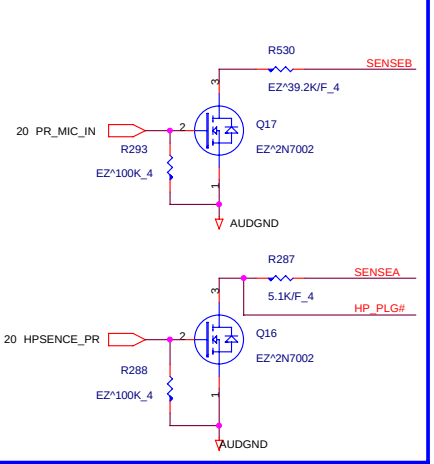


Audio Jack

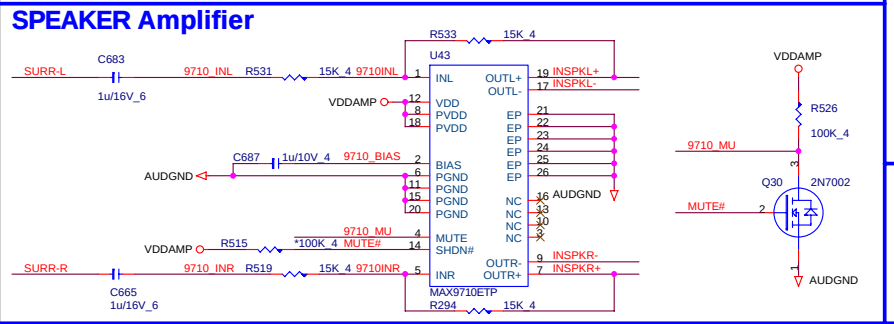


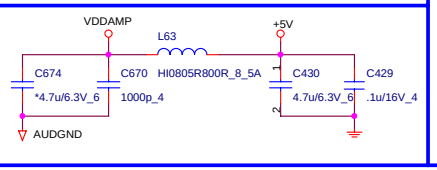
LINE OUT Amplifier

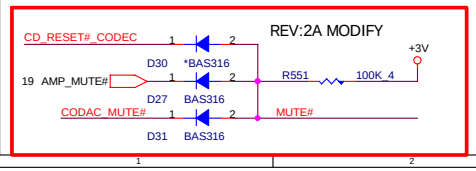


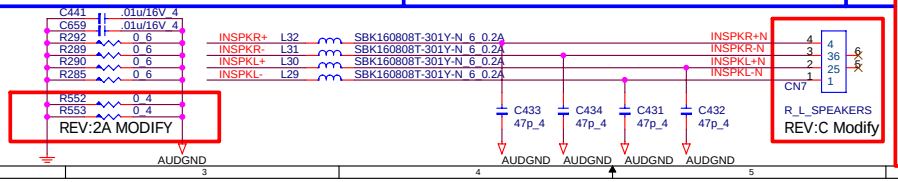


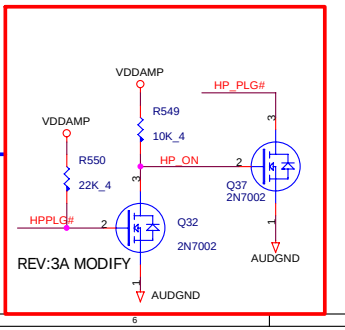
SPEAKER Amplifier











PROJECT : ZR1
Quanta Computer Inc.

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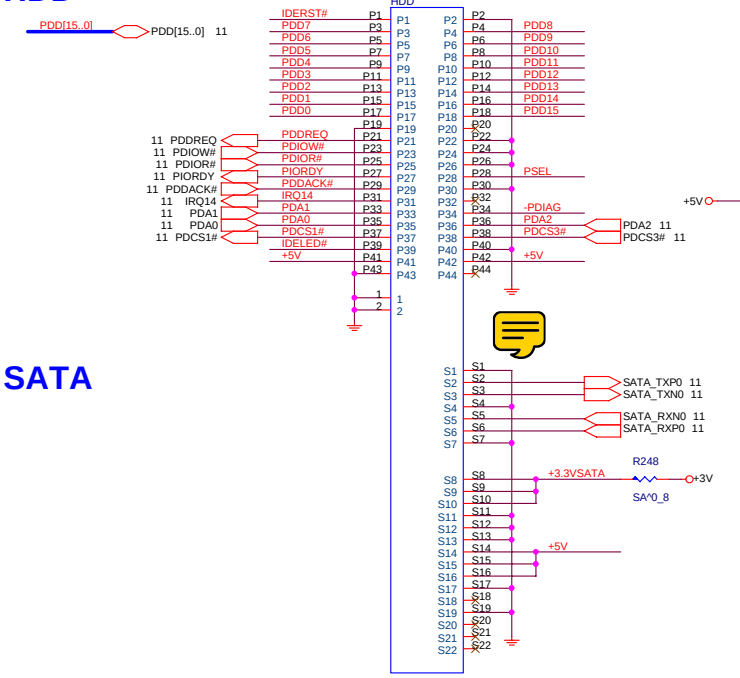
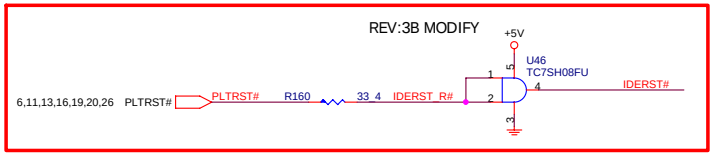
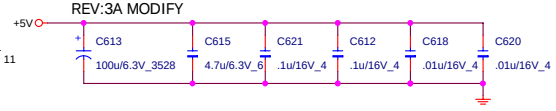
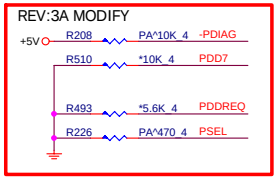
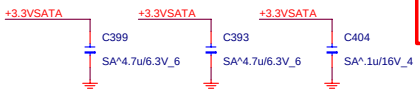
HDD



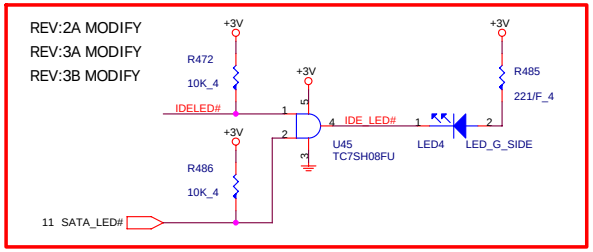




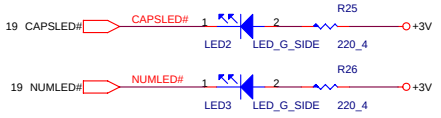


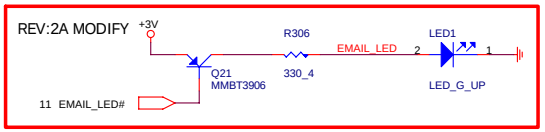


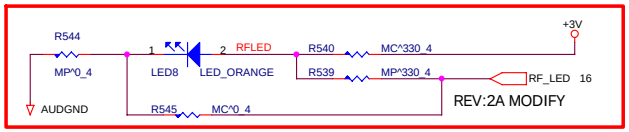
SATA



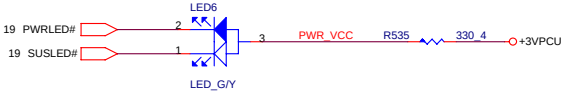
LED



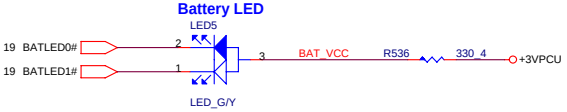




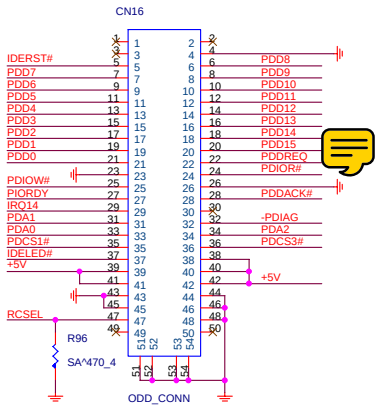
Power LED

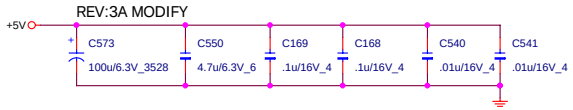


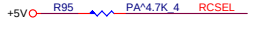
Battery LED



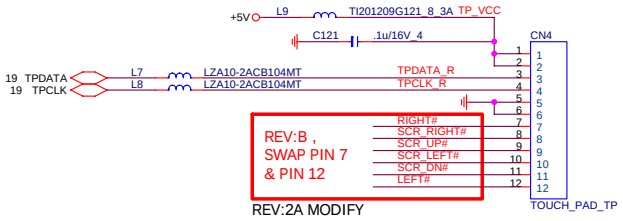
ODD



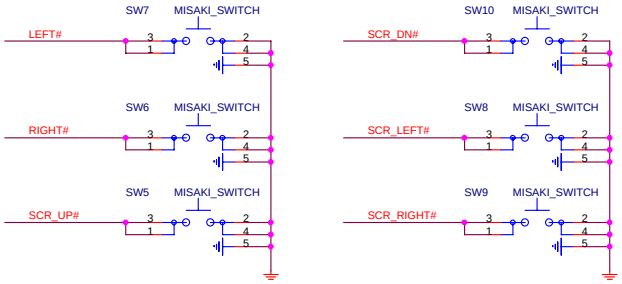




TP CONN

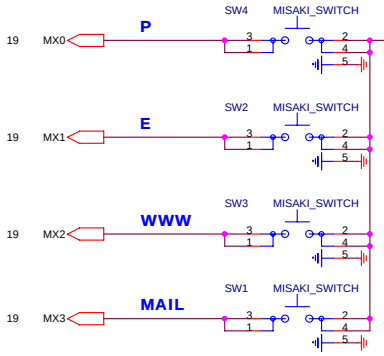


TP SWITCH



WL & BT SWITCH

QUICK KEY SWITCH



PROJECT : ZR1
Quanta Computer Inc.

Size	Document Number	Rev
	HDD/ODD/LED/SW/TP	3A
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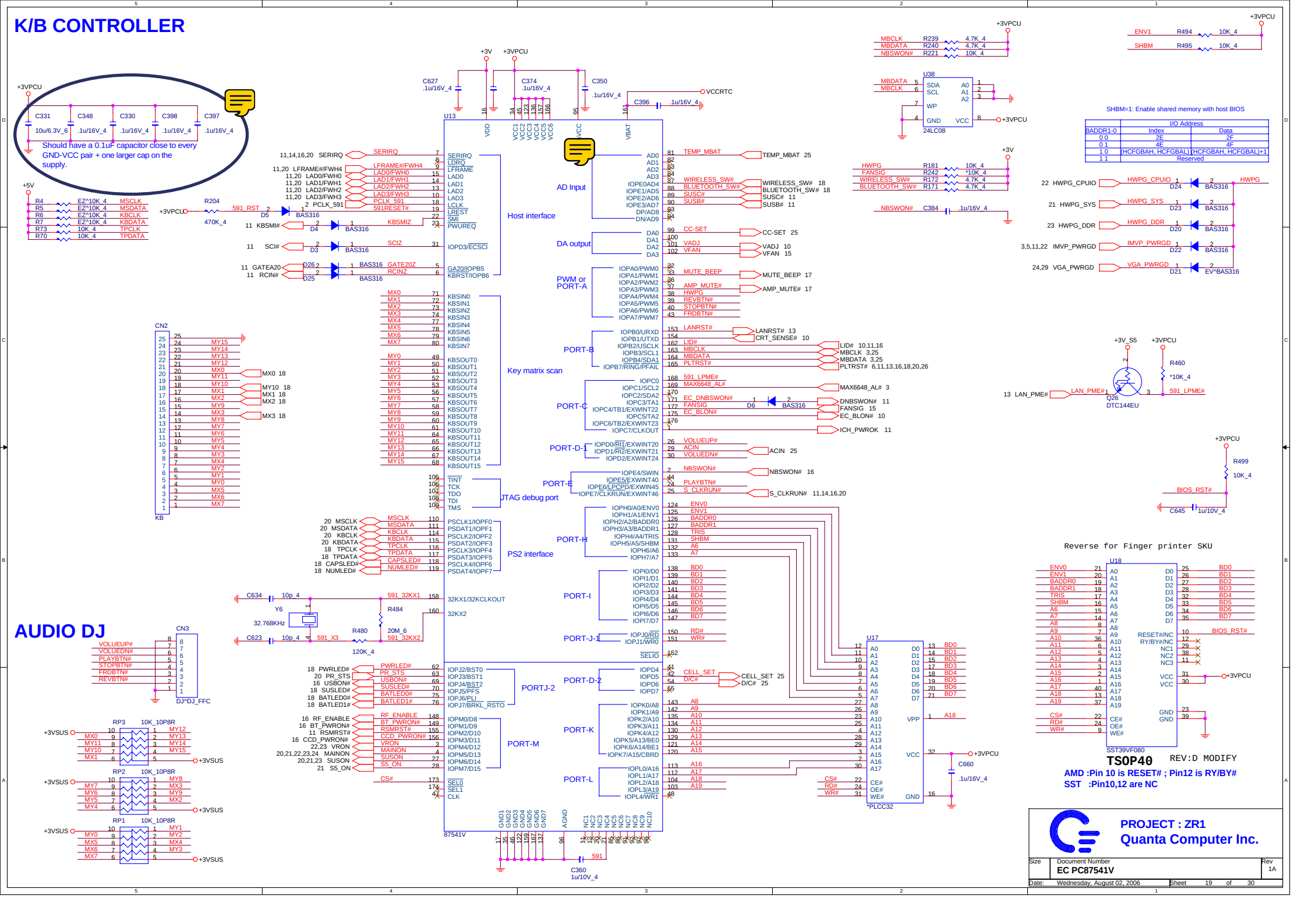
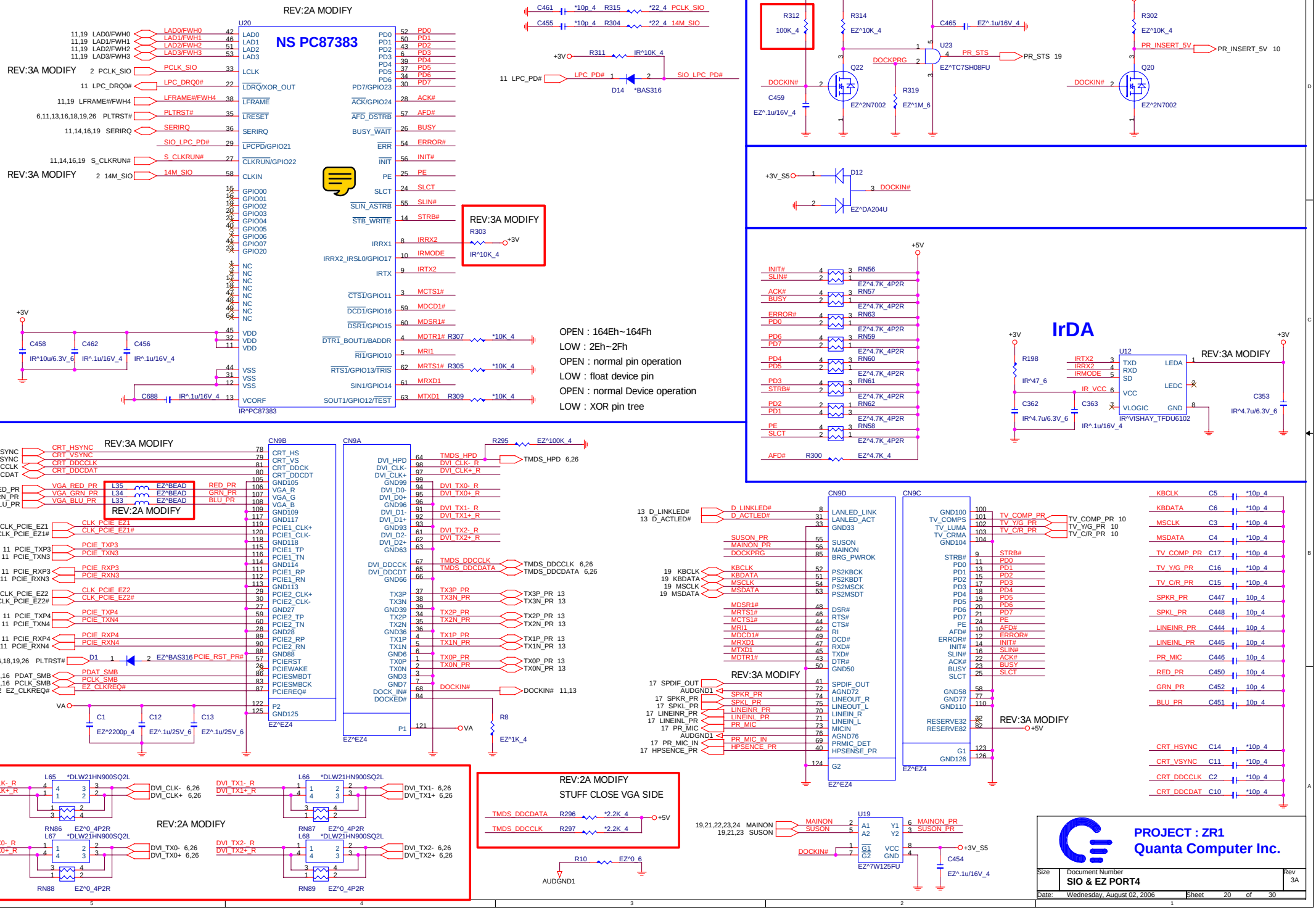
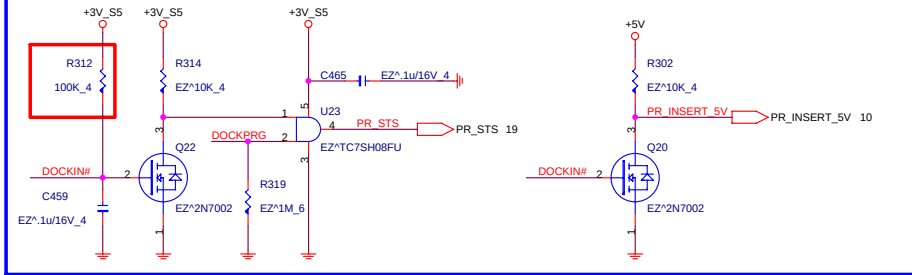
K/B CONTROLLER

AUDIO DJ



NS SIO 87383





EZ4

PROJECT : ZR1
Quanta Computer Inc.

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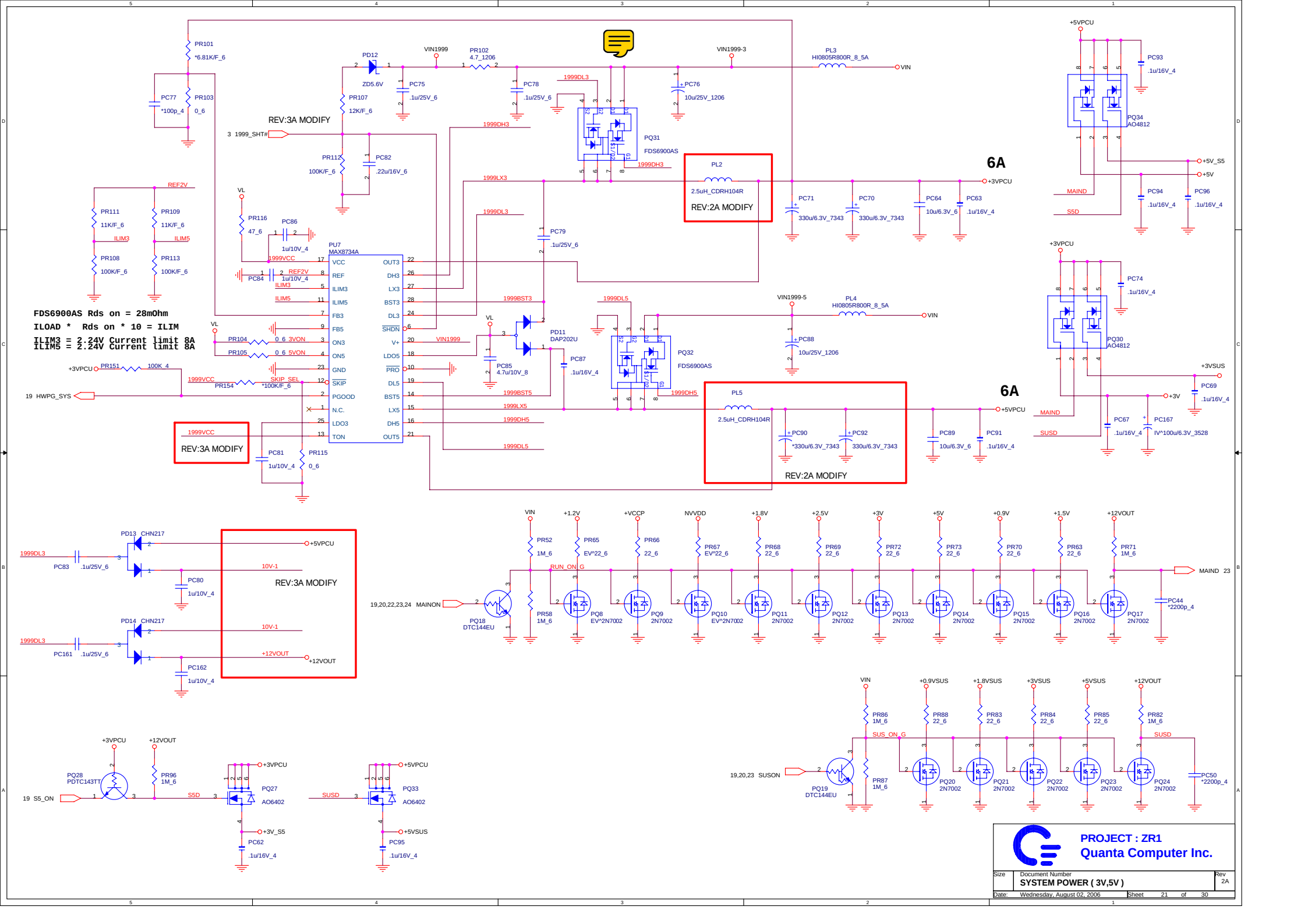
	SIO & EZ PORT4	3A
Date: Wednesday, August 02, 2006	Sheet 20 of 30	


FDS6900AS Rds on = 28mOhm
ILOAD * Rds on * 10 = ILIM
ILIM3 = 2.24V Current limit 8A
ILIM5 = 2.24V Current limit 8A
REV:3A MODIFY
REV:2A MODIFY
6A
PU7 MAX8734A
PROJECT : ZR1
Quanta Computer Inc.
SYSTEM POWER (3V,5V)
Rev 2A
Wednesday, August 02, 2006
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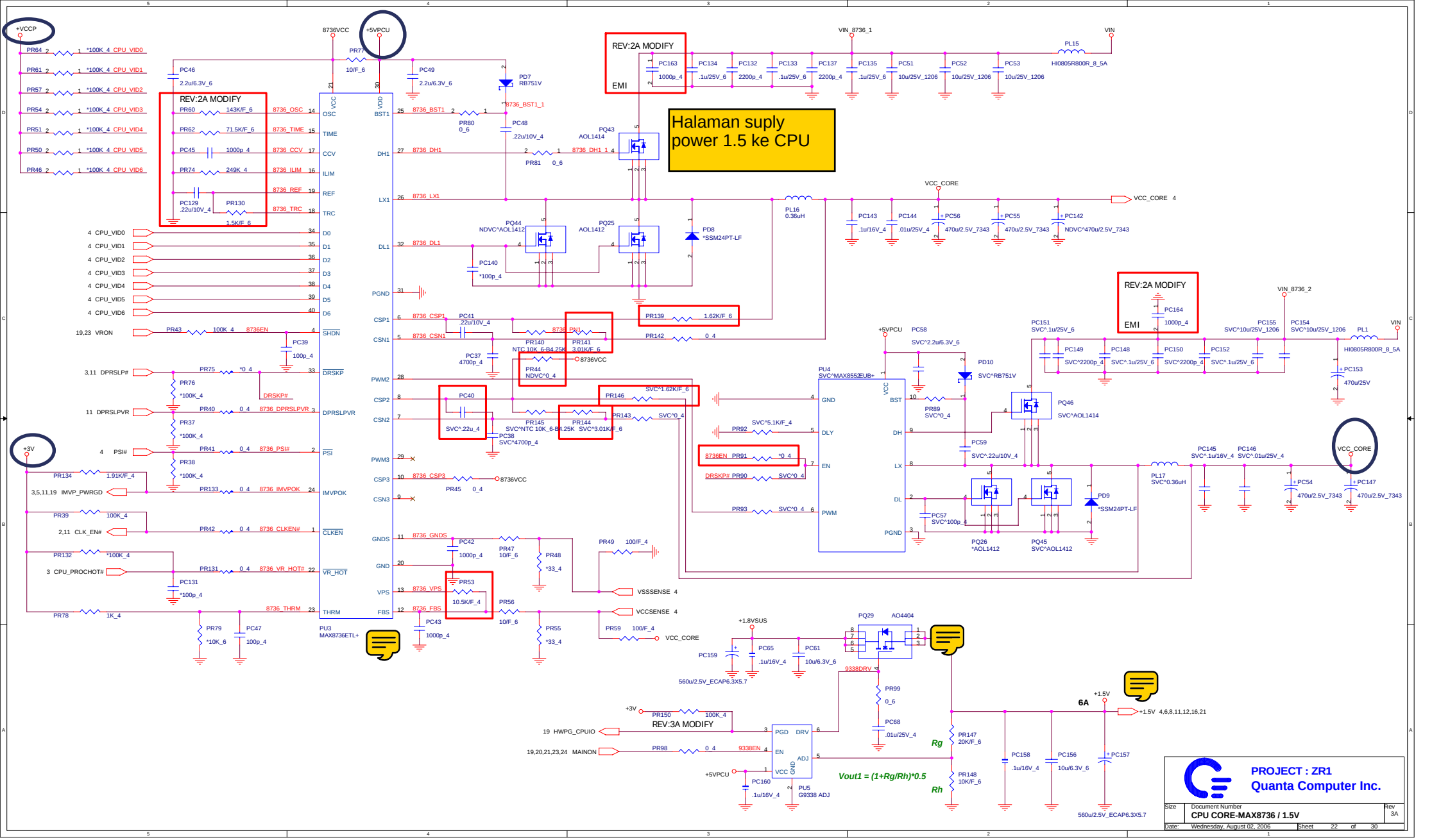
Halaman suply power 1.5 ke CPU
REV:2A MODIFY
EMI
REV:3A MODIFY
MAX8736ETL+
PROJECT : ZR1
Quanta Computer Inc.
CPU CORE-MAX8736 / 1.5V
Rev 3A
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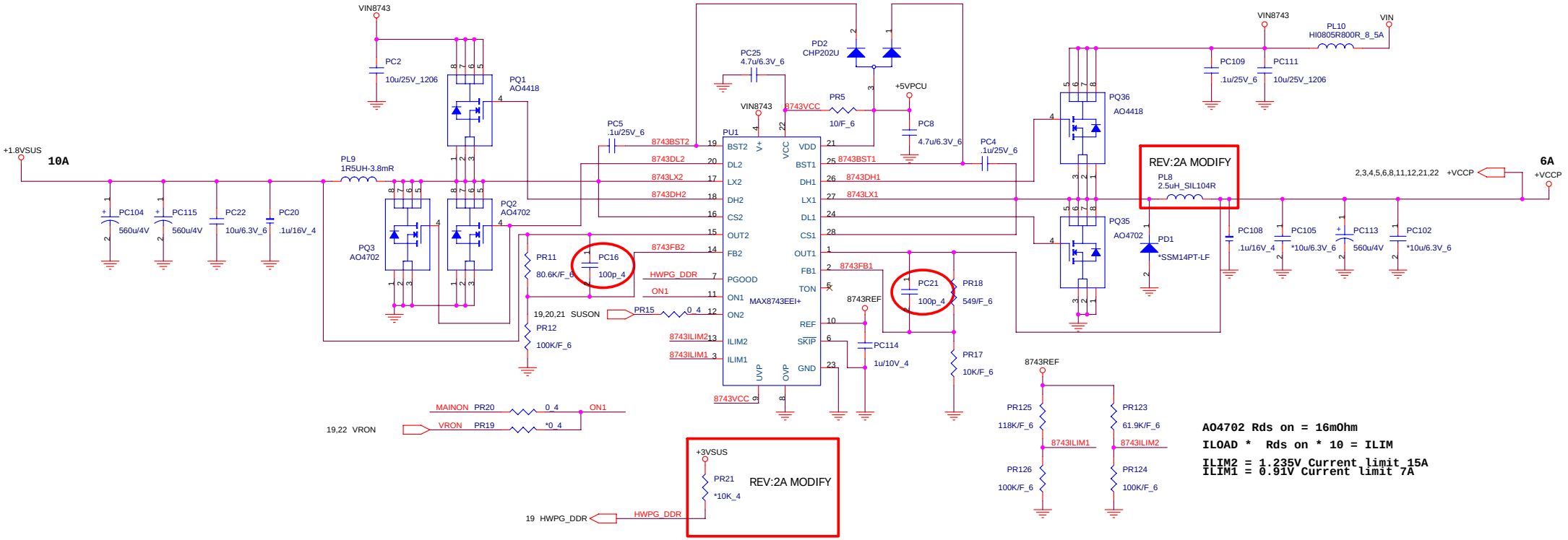

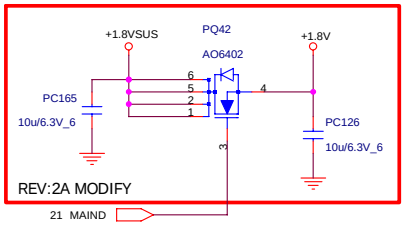

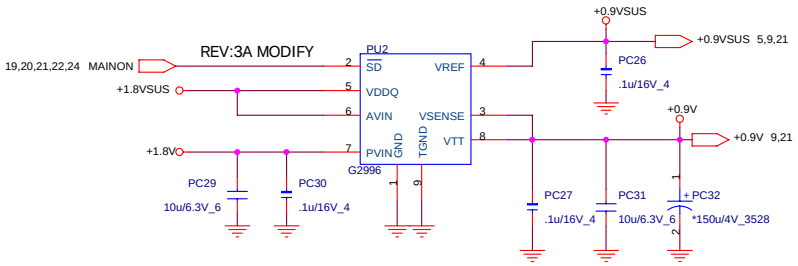

PROJECT : ZR1
Quanta Computer Inc.

Size	Document Number	Rev
	1.8VSUS / +VCCP / 0.9V	3A

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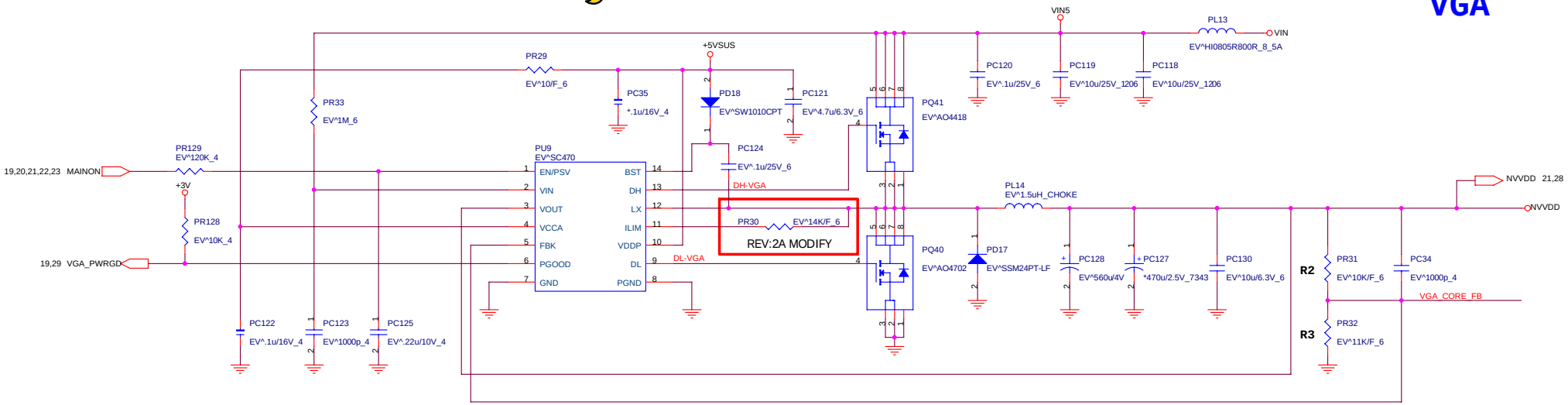
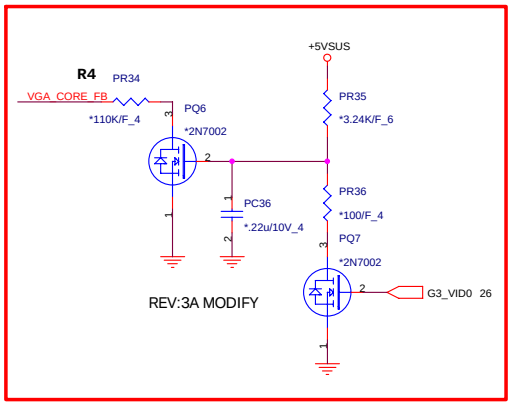
VGA





HI-->VOUT=(1+R2/R3)*0.5

LO-->VOUT=(1+R2/(R3//R4))*0.5

M52P(G)

PR1 : 10K

PR4 : 11K

PR2 : 110K

M54P

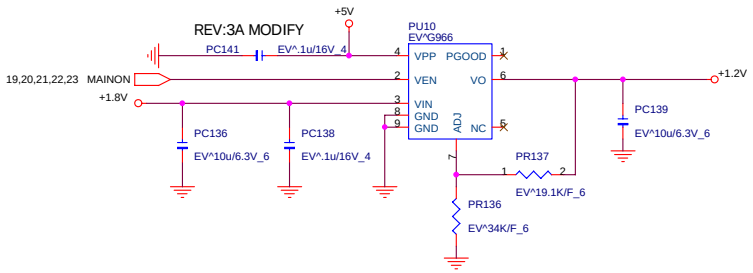
PR1 : 12K

PR4 : 12K

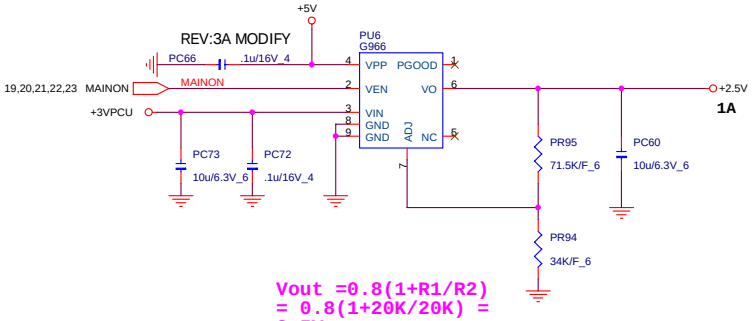
PR2 : 60.4K

Power Play Mode

VGA_PWR_SW	VGA_CORE
HI	0.95V--M52P(G) 1.0V --M54P
Default LO	1.0V--M52P(G) 1.1V--M54P



Vout =0.8(1+R1/R2) = 0.8(1+20K/20K) = 1.2V



Vout =0.8(1+R1/R2) = 0.8(1+20K/20K) = 2.5V

PROJECT : ZR1

Quanta Computer Inc.

Size	Document Number	Rev
	VGA (1.0V/1.2V/2.5V)	3A

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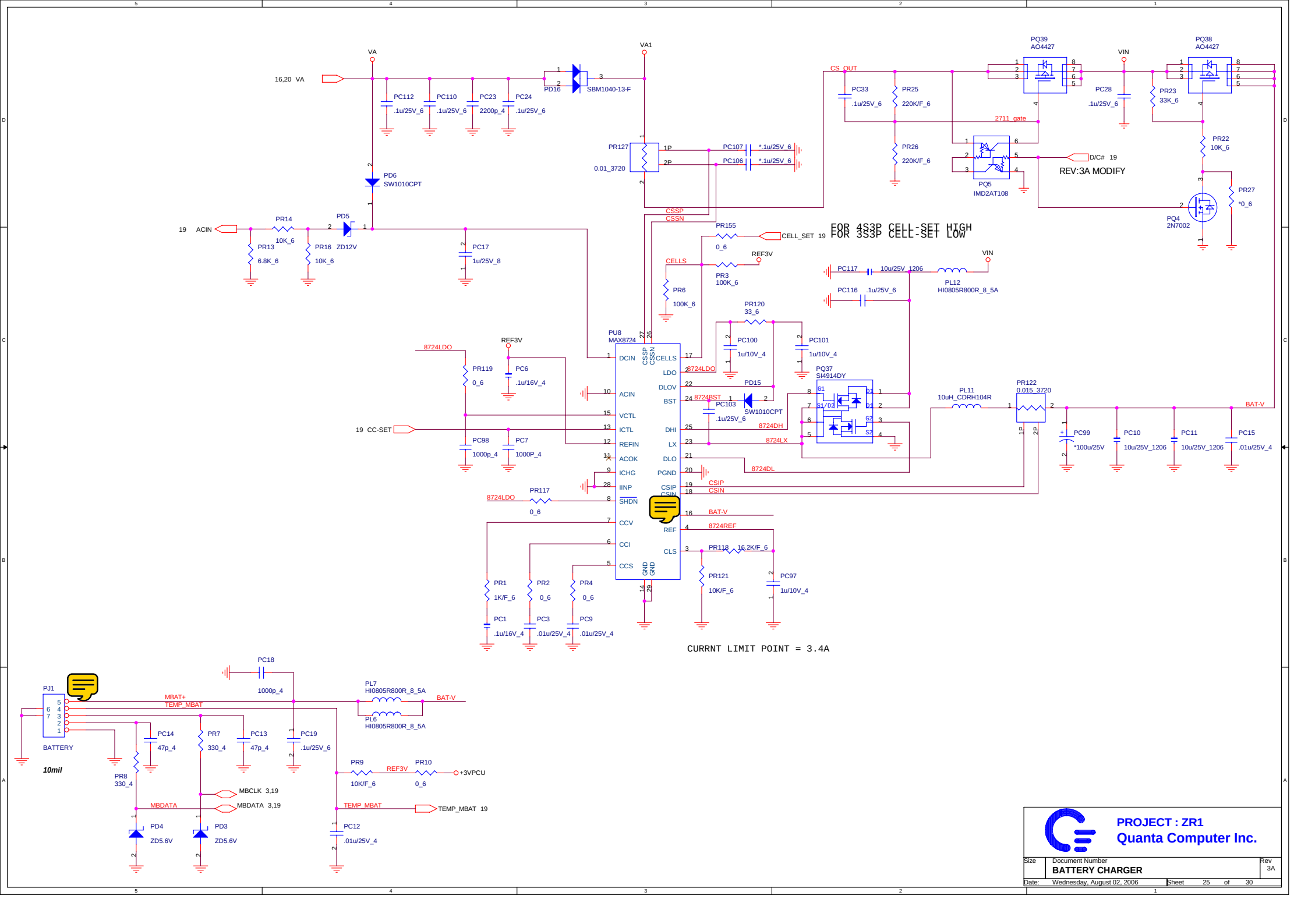
VA
VA1
16,20 VA
PC112 .1u/25V_6
PC110 .1u/25V_6
PC23 2200p_4
PC24 .1u/25V_6
PD16
SBM1040-13-F
PR127 0.01_3720
PC107 *.1u/25V_6
PC106 *.1u/25V_6
CSSP
CSSN
PD6 SW1010CPT
19 ACIN
PR14 10K_6
PD5
PR13 6.8K_6
PR16 10K_6
ZD12V
PC17 1u/25V_8
CS_OUT
PC33 .1u/25V_6
PR25 220K/F_6
PR26 220K/F_6
PQ39 AO4427
PQ5 IMD2AT108
2711_gate
D/C# 19
REV:3A MODIFY
VIN
PC28 .1u/25V_6
PQ38 AO4427
PR23 33K_6
PR22 10K_6
PR27 *0_6
PQ4 2N7002
FOR 4S3P CELL-SET HIGH
FOR 3S3P CELL-SET LOW
CELL_SET 19
PR155 0_6
CELLS
REF3V
PR3 100K_6
PR6 100K_6
PC117 10u/25V_1206
PC116 .1u/25V_6
PL12 HI0805R800R_8_5A
PR120 33_6
PC100 1u/10V_4
PC101 1u/10V_4
PU8 MAX8724
8724LDO
PR119 0_6
PC6 .1u/16V_4
19 CC-SET
PC98 1000p_4
PC7 1000P_4
PD15
8724BST
PC103 .1u/25V_6
SW1010CPT
PQ37 SI4914DY
8724DH
8724LX
8724DL
PL11 10uH_CDRH104R
PR122 0.015_3720
BAT-V
PC99 *100u/25V
PC10 10u/25V_1206
PC11 10u/25V_1206
PC15 .01u/25V_4
CSIP
CSIN
PR117 0_6
BAT-V
8724REF
PR118 16.2K/F_6
PR121 10K/F_6
PC97 1u/10V_4
PR1 1K/F_6
PR2 0_6
PR4 0_6
PC1 .1u/16V_4
PC3 .01u/25V_4
PC9 .01u/25V_4
CURRNT LIMIT POINT = 3.4A
PC18 1000p_4
PJ1
BATTERY
10mil
MBAT+
TEMP_MBAT
PL7 HI0805R800R_8_5A
PL6 HI0805R800R_8_5A
PC14 47p_4
PR7 330_4
PC13 47p_4
PC19 .1u/25V_6
PR8 330_4
PR9 10K/F_6
PR10 0_6
+3VPCU
MBCLK 3,19
MBDATA
MBDATA 3,19
TEMP_MBAT
TEMP_MBAT 19
PD4 ZD5.6V
PD3 ZD5.6V
PC12 .01u/25V_4
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Quanta Computer Inc.
Size
Document Number
BATTERY CHARGER
Rev 3A
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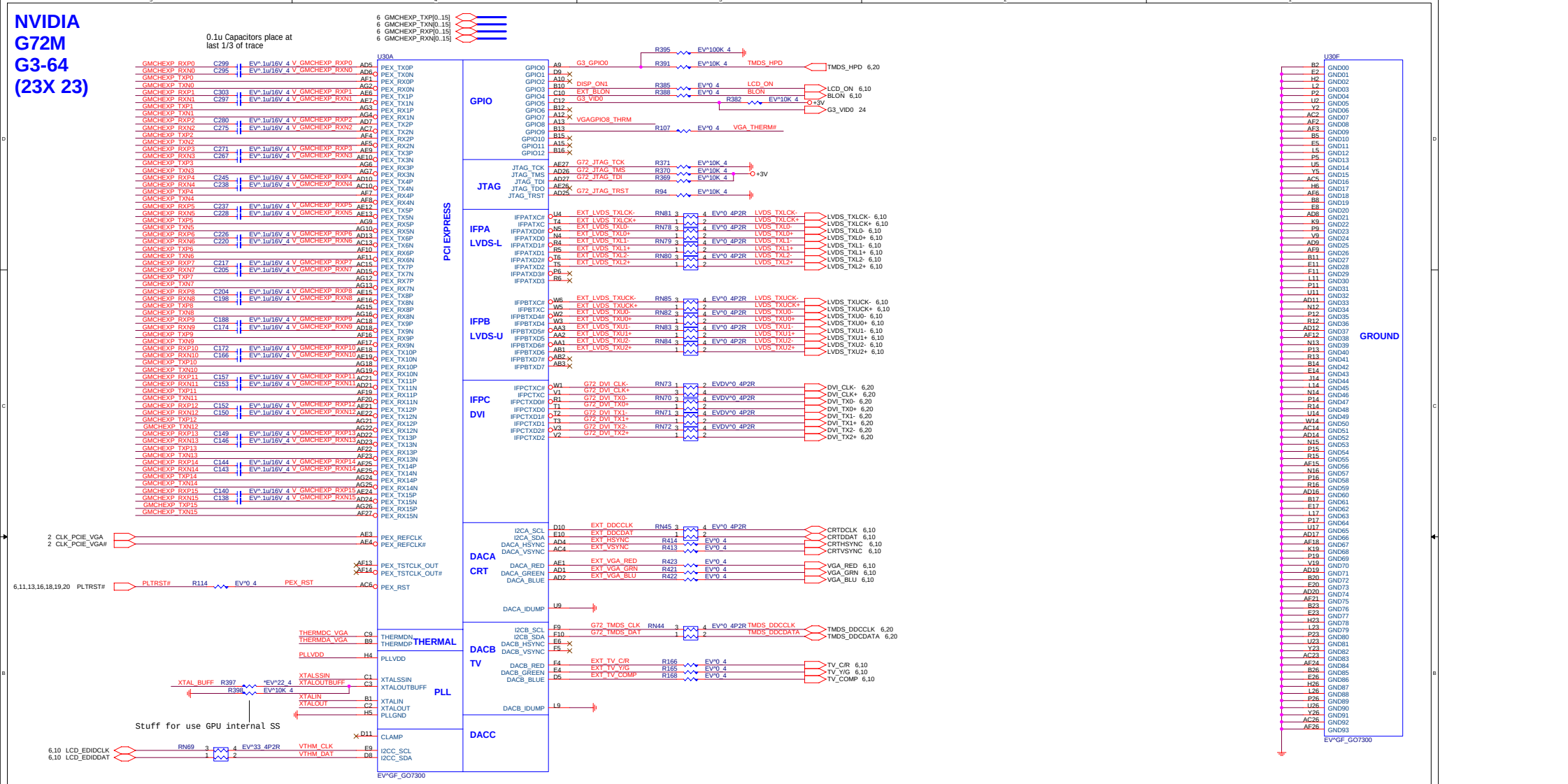
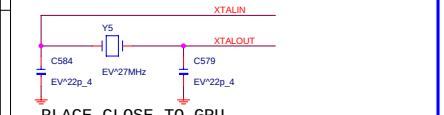
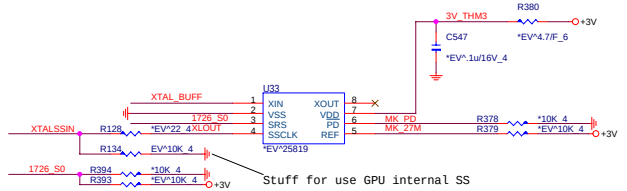
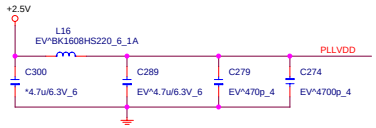
NVIDIA G72M G3-64 (23X 23)





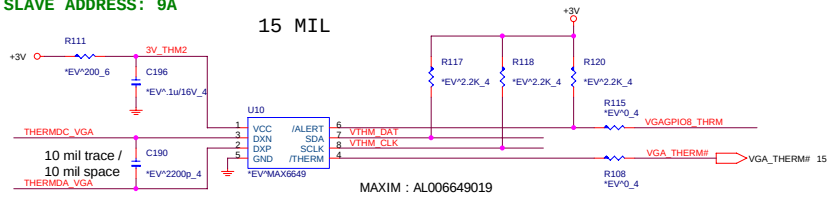
MEMORY CLOCK SPREAD SPECTRUM

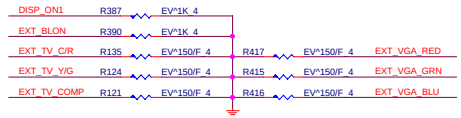




Thermal Sensor for Graphic

SLAVE ADDRESS: 9A





PROJECT : ZR1
Quanta Computer Inc.

Size	Document Number	Rev
	G72MV_PCIE/DAC/GND	1A
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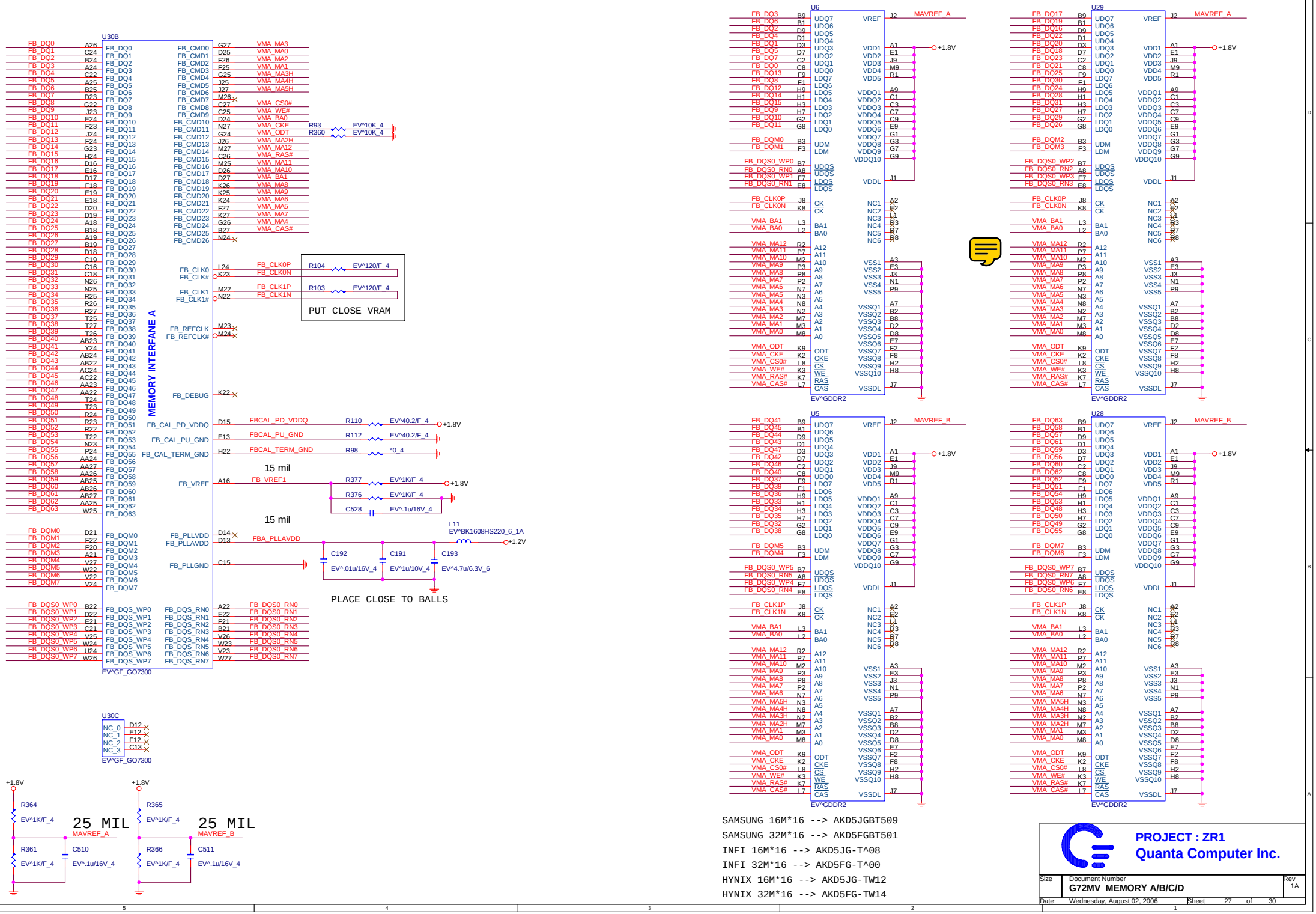
U30B
MEMORY INTERFANE A
EV^GF_GO7300
U30C
NC_0
NC_1
NC_2
NC_3
EV^GF_GO7300
PUT CLOSE VRAM
PLACE CLOSE TO BALLS
15 mil
15 mil
25 MIL
MAVREF_A
25 MIL
MAVREF_B
U6
U5
U29
U28
EV^GDDR2
SAMSUNG 16M*16 --> AKD5JGBT509
SAMSUNG 32M*16 --> AKD5FGBT501
INFI 16M*16 --> AKD5JG-T^08
INFI 32M*16 --> AKD5FG-T^00
HYNIX 16M*16 --> AKD5JG-TW12
HYNIX 32M*16 --> AKD5FG-TW14
PROJECT : ZR1
Quanta Computer Inc.
G72MV_MEMORY A/B/C/D
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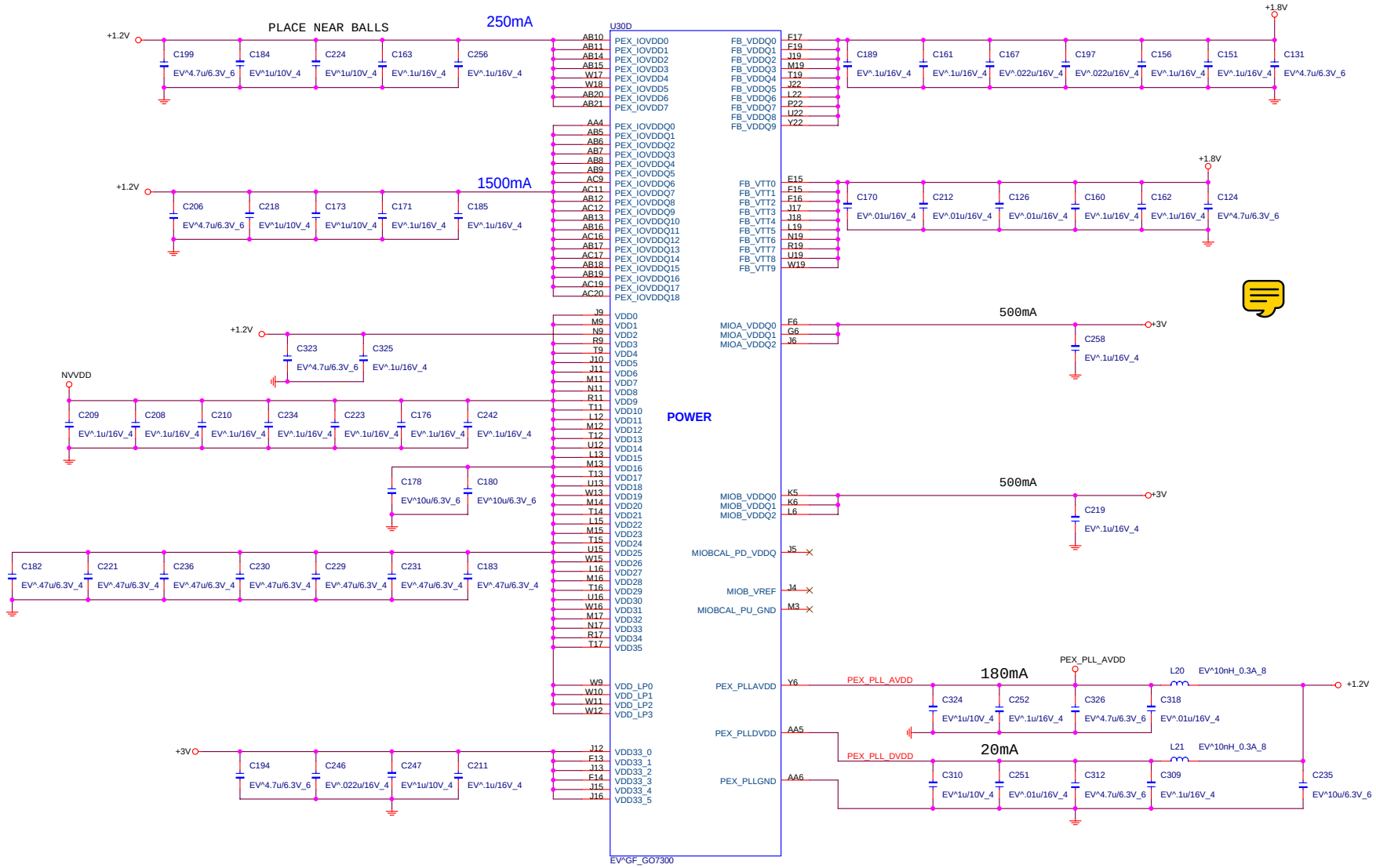

PROJECT : ZR1
Quanta Computer Inc.

Size	Document Number	Rev
	G72MV_POWER	1A

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